

```
UJN          = CPUMLD      FAMILY      = CYBER      JOB ORIGIN    = INTERACTIVE.
CREATING JSN = AABY        USER NAME  = INSTALL    SERVICE CLASS = INTERACTIVE.
```

1 *EDIT CPUMLD

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1412THE

COPYRT MODIFIERS.

NS2796

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3	4 ACTIVE LINE(S). 1 INACTIVE LINE(S). 1 INSERTED LINE(S).							3
4								4
5	CPUMLD MODIFIERS.							5
6								6
7	NS21000	NS22000	V23L617	NS2332	251L664	271L716	281L803	7
8	NS2086	V22L602	NS2241	CPUMLD1	252L678	CPUMLD2	283L840	8
9								9
10	*CALL,COMCMAC					CPUMLD	55	10
11	*CALL COMSDSL					CPUMLD	57	11
12	*CALL COMCSYS					CPUMLD	551	12
13	*CALL COMCCIO					CPUMLD	1286	13
14								14
15	1731 ACTIVE LINE(S). 157 INACTIVE LINE(S). 295 INSERTED LINE(S).							15
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DECKS ON PROGRAM LIBRARY.

	COPYRT	COMCSFN	COMP2D	COMPVID	COMSWEI	0ST	IHFU	TDUMP	COMFXFO	COMXSEB	EORSS15	SUBMT
1	CETEXT	COMCSKW	COMPDDT	COMPVLC	COMSZOL	0VJ	INSTALL	TDUOUT	COMFXSB	COMTALT	M86FORM	TARO
2	ECSTEXT	COMCSNF	COMPDLI	COMPVMS	COMS0VU	0VU	ISF	TDUTAB	COMFXSC	COMTBLD	M86SERV	TERMDEF
3	PPTEXT	COMCSNM	COMPPTS	COMPVPA	COMS1DS	1AJ	KEY	TERMIO	COMFXWK	COMTBLP	EORSS16	TSIM
4	PSSTEXT	COMCSOE	COMPVVC	COMPVSP	COMS1MV	1CD	KEYEX	TRMDEF	FSEBUFF	COMTCTW	VERMSGC	TSTAT
5	NOSTEXT	COMCSRI	COMPV5	COMPWBB	COMS1RM	1CK	KEYPAN	ULIB	FSECMDS	COMTDBG	EORSS17	WAIT
6	SSYTEXT	COMCSRT	COMPECX	COMPWCB	COMS176	1CL	KRONREF	VALEX	FSEDATA	COMTDBP	MSE	WAITINP
7	SYSTEXT	COMCSSN	COMPFAT	COMPWEI	COMTBAN	1DL	LDI	VALNET	FSEEDIT	COMTDEF	MSESLAV	WSTAT
8	CPCOM	COMCSST	COMPFLF	COMPWSS	COMTCVT	1DS	LIBEDIT	VCC	FSEEX	COMTDER	MSECONF	BTASK
9	PPCOM	COMCSTF	COMPGBN	COMPWVE	COMTDA8	1HY	LIBGEN	VDTSUBS	FSEFILE	COMTDFP	EORSS18	CRMTASK
10	COMCMAC	COMCSYS	COMPGBP	COMSACC	COMTDP6	1IO	LIBRARY	VERIFY	FSEFORM	COMTERR	SSCONTL	CTASK
11	COMCCMD	COMCTIO	COMPBTN	COMSATF	COMTDP9	1IS	LIDOU	VFYLIB	FSEHELP	COMTFMT	FREEDSK	ITASK
12	COMABZF	COMCUPC	COMPICT	COMSBIO	COMTDSP	1LC	LISTLB	VIRTERM	FSELIB	COMTLAB	DESTAGE	KDIS
13	COMAFET	COMCUSB	COMPIFR	COMSCIO	COMTNAP	1MA	LISTLID	MAC1	FSEMAIN	COMTLBP	EORSS19	LOGT
14	COMAMSS	COMCVDE	COMPIMB	COMSCPD	COMTVDT	1MB	LIST80	MAC2	FSEPROC	COMTMOV	ISHARED	MSABT
15	COMAPFP	COMCVDT	COMPIOU	COMSCPS	COMT6DP	1MC	LOADBC	RFORM	FSESCRN	COMTMVD	COMKMAC	OFFTASK
16	COMAPFS	COMCVLC	COMPIRA	COMSCVS	COMT8AD	1MD	L072	SYMSERV	FSESUBS	COMTMVP	COMKARF	RCTASK
17	COMCARG	COMCVQF	COMPLDA	COMSDFS	COMT9DP	1MI	MAG	CPUREL	FSETABL	COMTOUT	COMKBRD	RTASK
18	COMCARM	COMCWOD	COMPLDB	COMSDFT	COMUCPD	1MS	MAGNET	APRINST	FSEWORK	COMTSIT	COMKBST	STASK
19	COMCBAN	COMCWTA	COMPMRA	COMSDSL	COMUEST	1MT	MFILES	CMRINST	FSTEACH	COMTUSE	COMKCBBD	SYMSG
20	COMCBLP	COMCWTC	COMPMRM	COMSDSP	COMUFMT	1MU	MLSEXEC	EQPINST	SMFEX	COMTUSP	COMKCBT	XTASK
21	COMCCCE	COMCWTH	COMPMRQ	COMSDST	COMUJCA	1MV	MODIFY	IPRINST	SMFSTAT	COMTVLD	COMKCRM	COMCCDM
22	COMCCDD	COMCWTO	COMPMSV	COMSEJT	COMUOUT	1PP	MODVAL	COMLBAS	SMF	COMTVLF	COMKDPB	COMCCDP
23	COMCCFD	COMCWTS	COMPNFL	COMSESS	COMUPRB	1RI	MSI	COMLESM	1HP	COMTVLM	COMKFIO	COMSSTM
24	COMCCHD	COMCWTW	COMPPDI	COMSEVT	COMUQPR	1RM	NOTE	COMLFLD	COMCLNI	COMTVLP	COMKFLD	ADC
25	COMCCHG	COMCZAP	COMPPPR	COMSHIO	COMUQQC	1RO	OPLEDIT	COMLIPR	IAFP	COMTVLV	COMKIPR	BAT
26	COMCCIO	COMCZTB	COMPRBB	COMSIOQ	CALLCPU	1SJ	PACK	COMLSCD	IAFTM	COMTVLX	COMKKIM	DCC
27	COMCCNS	COMDMAC	COMPRCB	COMSIOU	CALLDIS	1TA	PANEL	COMLUEM	IAFTR	CALLFAS	COMKNWC	DDF
28	COMCCOD	COMDDBS	COMPRCS	COMSJCE	CALLPPU	1TM	PANSUBS	COMLVER	1TN	1SS	COMKNWF	DOG
29	COMCCPA	COMDDCM	COMPRI	COMSJIO	CALLSYS	1TO	PDU	APRDECK	RECOVER	EORSS1	COMKOPD	DS1
30	COMCCPM	COMDDIS	COMPREL	COMSJRO	CALLTAB	1VP	PFAM	CMRDECK	0MF	GMSG	COMKRRD	HFM
31	COMCCPT	COMDDSP	COMPRFI	COMSLFD	CALLINT	1XD	PFDM	EQPDECK	1MR	SMSG	COMKSCD	MPF
32	COMCCUA	COMDD7S	COMPRJC	COMSLFM	CPM	1XM	PFDUMP	IPRDECK	MREC	CALPFU	COMKSTC	SMP
33	COMCCVI	COMDGJD	COMPRLA	COMSLSD	CVL	1XY	PFHELPR	LIBDECK	MTE	GETMST	COMKTAF	WRM
34	COMCCVL	COMDSYS	COMPRLI	COMSMLS	DIO	5ME	PFILES	RDFP	COMBFAS	SETQP	COMKTD	1TS
35	COMCDCM	COMDTFN	COMPRLM	COMSMMF	DIS	6DC	PFLIST	SUPERM	COMBBZF	EORSS2	COMKTER	DDFILE
36	COMCDCP	COMFCID	COMPRLS	COMSMRT	DSD	6DD	PFLOAD	VEMEM	COMBCDD	SSOVL	COMKTIF	DEMUX
37	COMCDTC	COMFVD2	COMPRNS	COMSMSC	DSP	6DE	PFS	ZTDAMT0	COMBCHN	SSARG	COMKTIP	DMPCCC
38	COMCDXB	COMFVD3	COMPRSI	COMSMSI	ELM	6DI	PROFILE	ZTDCCLC	COMBCMD	EORSS3	COMKTLD	KEYUTIL
39	COMCECM	COMFXTI	COMPRSS	COMSMSP	FDL	6DP	PURGALL	ZTDCCON	COMBCMS	SSEXEC	COMKTRF	LPT
40	COMCECS	COMFXVT	COMPSAF	COMSMST	IMS	6DX	QDSPLAY	ZTDCERR	COMBCPR	EORSS4	COMKTRN	MST
41	COMCEDT	COMFPAN	COMPSCA	COMSMTR	LFM	BATCHIO	QDUMP	ZTDCVRB	COMBFET	SSSLV	COMKTS	PACKER
42	COMCFCE	COMFTIO	COMPSDA	COMSMTX	MDD	BLANK	QFSP	ZTDNMT0	COMBHFC	EORSS5	COMKTSC	SCRSIM
43	COMCFLD	COMFVDT	COMPSDI	COMSNCD	MSM	CATALOG	QFTLIST	ZTDPCLP	COMBKDA	ACCCAT	COMKTST	STIMULA
44	COMCFQO	COMFVD1	COMPSDN	COMSNET	MTR	CATLIST	QGET	ZTDPERR	COMBKDD	ACCMAP	COMKZFN	TST
45	COMCGMS	COMPMAC	COMPSDR	COMSPDT	OSB	CHKPT	QLOAD	ZTDPFIL	COMBLBL	SSINIT	TAFPRC	DFSORT
46	COMCGTO	COMPACS	COMPSEI	COMSPFM	O26	CLASS	QMOVE	ZTDPTBD	COMBLRQ	EORSS6	CALLKTS	PSAMP
47	COMCHXB	COMPANS	COMPSES	COMSPFS	PFM	CLDT	QREC	ZTDPTBS	COMBMAP	SSALTER	KTSAMP	SECART
48	COMCIQP	COMPAPI	COMPSFB	COMSPFU	PFU	CODING	RECLAIM	ZTDTFIL	COMBMAT	EORSS7	LIBTASK	MSGID
49	COMCJCR	COMPAST	COMPSFE	COMSPIM	PPR	CONFIG	REDO	ZTDTTAB	COMBMCT	SSBLD	TAFLOG	ABC
50	COMCLFM	COMPCDB	COMPSFI	COMSPRD	QAC	CONTROL	RESEQ	ZTDVERB	COMBOVL	EORSS8	TAF	CHD
51	COMCLOD	COMPCDD	COMPSFN	COMSPRO	QAP	COPYB	RESEX	ZTDVMT0	COMBPFP	SSDEBUG	TAFREC	DEBUG
52	COMCMSF	COMPCEA	COMPSIC	COMSQAC	QFM	COPYC	RESTART	ZTDVPDT	COMBPFS	EORSS9	BAAML	CPD
53	COMCMTM	COMPCFP	COMPSMI	COMSQFS	REC	CPMEM	ROUTE	5870JDL	COMBRCD	SSDEF	DMREC	ICPD
54	COMCMTP	COMPCHD	COMPSNT	COMSREM	RPV	CPUMLD	SCREX	EOR1	COMBSIT	EORSS10	TARL	ACPD
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	COMCMVE	COMPCHI	COMPSOF	COMSRPV	SET	CPUMTR	SCTD	EOR2	COMBSNS	SSLABEL	TMSG	PROBE
	COMCOVL	COMPCHL	COMPSPA	COMSRSX	SFM	CPUPFM	SDSPLAY	EOR3	COMBTDM	EORSS11	AAMI	XEDIT
	COMCPFM	COMPCHM	COMPSRA	COMSSCD	SFP	CUESHEL	SECHDR	EOR4	COMBUCR	SSMOVE	AAML	XEDITH
1	COMCPFP	COMP CIB	COMPSRR	COMSSCP	SLL	CVLCP	SETCORE	EOR5	COMBUDT	EORSS12	BEGIN	1DA
2	COMCPFS	COMPCLC	COMPSRU	COMSSCR	STL	DAYFILE	SFORM	EOR6	COMXACM	SSUSE	BLDABH	0CT
3	COMCPFU	COMPCKP	COMPSSE	COMSSFm	TLX	DFTERM	SFS	EOR7	COMXBST	EORSS13	CALLRTN	COMCCKD
4	COMCPOP	COMPCLD	COMPSSF	COMSSFS	VEJ	DOCUMENT	SHOW	EOR8	COMXCCB	SSVAL	CALLTRN	COMCMBS
5	COMCQFM	COMPCLX	COMPSTA	COMSSRT	VER	DSDI	SHOWEX	EOR9	COMXCTF	EORSS14	CALLTSK	COMPTFM
6	COMCQFP	COMP CMA	COMPSTI	COMSSRU	XHC	EDIT	SMFSUBS	EOR10	COMXEMC	EXDRVr	CEASE	COMSTFU
7	COMCRDA	COMP CMX	COMPSUD	COMSSSD	0AU	ENQUIRE	SORT	COMFDS1	COMXEXP	SXDEST	CHKON	TFM
8	COMCRDC	COMP COB	COMPSUT	COMSSSE	0AV	FCOPY	STAGE	COMFDS2	COMXFCQ	SXHLR	CMDUMP	TFU
9	COMCRDH	COMP CPE	COMPTGB	COMSSSJ	0BF	FILES	SUBMIT	COMFFSE	COMXHLLR	SXINIT	DSDUMP	TFILES
10	COMCRDO	COMP CRA	COMPTLB	COMSTCM	0DF	FOTD	SUBSYST	COMFMLT	COMXINT	SXKD	EXTRACT	TFSP
11	COMCRDS	COMP CRS	COMPTMA	COMSTDR	0DQ	GENPFD	SYMPCOD	COMFONL	COMXIPR	SXLLR	INTOT	LDISTAP
12	COMCRDW	COMP CSC	COMPUFT	COMSTFM	0FA	GTR	SYSEDIT	COMFSGL	COMXJCA	SXMAIN	JOURNL	GETTASV
13	COMCRSB	COMP CTE	COMPUPP	COMSTIO	0PT	HELPLIB	TCOMND	COMFSMF	COMXLTC	SXSERV	LIMITS	SETTASV
14	COMCRSP	COMP CTI	COMPUPS	COMSTIR	0QM	HOSTCPY	TDU	COMFTAB	COMXMFD	SXSTGE	LOGIN	TMSPROC
15	COMCRTN	COMP CUA	COMPVEI	COMSTRX	0RF	HSTCOPY	TDUEx	COMFXCM	COMXMMF	SXSLV	MULTCB	TMSPROG
16	COMCSCB	COMP CUT	COMPVFC	COMSVED	0RP	IAFEX	TDUFILE	COMFXED	COMXMSC	SXUCP	SEND	
17	COMCSFM	COMP CVI	COMPVFN	COMSVER	0RT	IEDIT	TDUIN	COMFXFL	COMXOVL	SX3UCP	SETCHT	

COMMON DECKS ON PROGRAM LIBRARY.

21	COPYRT	COMCGTO	COMCSYS	COMPAST	COMPIFR	COMPSIC	COMSCVS	COMSRPV	COMUEST	COMFXED	COMXHLLR	COMKBST
22	CPCOM	COMCHXB	COMCTIO	COMP CDB	COMPIMB	COMPSMI	COMSDFS	COMSRSX	COMUFMT	COMFXFL	COMXINT	COMK CBD
23	PPCOM	COMCIQP	COMCUPC	COMP CDD	COMPIOU	COMPSNT	COMSDFT	COMSSCD	COMUJCA	COMFXFO	COMXIPR	COMK CBT
24	COMCMAC	COMCJCR	COMCUSB	COMPCEA	COMPIRA	COMPSOF	COMSDSL	COMSSCP	COMUOUT	COMFXSB	COMXJCA	COMKCRM
25	COMCCMD	COMCLFM	COMCVDE	COMP CFP	COMPLDA	COMPSPA	COMSDSP	COMSSCR	COMUPRB	COMFXSC	COMXLTC	COMKDPB
26	COMABZF	COMCLOD	COMCVDT	COMP CHD	COMPLDB	COMP SRA	COMSDST	COMSSFm	COMUQPR	COMFXWK	COMXMFD	COMKFIO
27	COMAFET	COMCMSF	COMCVLC	COMP CHI	COMPMRA	COMPSRR	COMSEJT	COMSSFS	COMUQQC	COMCLNI	COMXMMF	COMKFLD
28	COMAMSS	COMCMTM	COMCVQF	COMP CHL	COMPMRM	COMPSRU	COMSESS	COMSSRT	COMLBAS	COMBFAS	COMXMSC	COMKIPR
29	COMAPFP	COMCMTP	COMCWOD	COMP CHM	COMPMRQ	COMPSSE	COMSEVT	COMSSRU	COMLESM	COMBBZF	COMXOVL	COMKKIM
30	COMAPFS	COMCMVE	COMCWTA	COMP CIB	COMPMSV	COMPSSF	COMSHIO	COMSSSD	COMLFLD	COMBCDD	COMXSEB	COMKNWC
31	COMCARG	COMCOVL	COMCWTC	COMPCLC	COMPNFL	COMPSTA	COMSIOQ	COMSSSE	COMLI PR	COMBCHN	COMTALT	COMKNWF
32	COMCARM	COMCPFM	COMCWTH	COMPCKP	COMPPDI	COMPSTI	COMSIOU	COMSSSJ	COMLSCD	COMBCMD	COMTBLD	COMKOPD
33	COMCBAN	COMCPFP	COMCWTO	COMPCLD	COMPPPR	COMPSUD	COMSJCE	COMSTCM	COMLUEM	COMBCMS	COMTBLP	COMKRRD
34	COMCBLP	COMCPFS	COMCWTS	COMPCLX	COMPRBB	COMPSUT	COMSJIO	COMSTDR	COMLVER	COMBCPR	COMTCTW	COMKSCD
35	COMCCCE	COMCPFU	COMCWTW	COMP CMA	COMPRCB	COMPTGB	COMSJRO	COMSTFM	ZTDAMT0	COMBFET	COMTDBG	COMKSTC
36	COMCCDD	COMCPOP	COMCZAP	COMP CMX	COMPRCS	COMPTLB	COMSLFD	COMSTIO	ZTDCCLC	COMBHFC	COMTDBP	COMKTAF
37	COMCCFD	COMCQFM	COMCZTB	COMP COB	COMP REI	COMPTMA	COMSLFM	COMSTIR	ZTDCCON	COMBKDA	COMTDEF	COMKTDM
38	COMCCHD	COMCQFP	COMDMAC	COMP CPE	COMP REL	COMPUFT	COMSLSD	COMSTRX	ZTDCERR	COMBKDD	COMTDER	COMKTER
39	COMCCHG	COMCRDA	COMDDBS	COMP CRA	COMP RFI	COMPUPP	COMSMLS	COMSVED	ZTDCVRB	COMBLBL	COMTDFP	COMKTIF
40	COMCCIO	COMCRDC	COMDDCM	COMP CRS	COMPRJC	COMPUPS	COMSMMF	COMSVER	ZTDNMT0	COMBLRQ	COMTERR	COMKTIP
41	COMCCNS	COMCRDH	COMDDIS	COMP CSC	COMPRLA	COMPVEI	COMSMRT	COMSWEI	ZTDPCLP	COMBMAP	COMTFMT	COMKTLD
42	COMCCOD	COMCRDO	COMDDSP	COMP CTE	COMPRLI	COMPVFC	COMSMSC	COMSZOL	ZTDPERR	COMBMAT	COMTLAB	COMKTRF
43	COMCCPA	COMCRDS	COMDD7S	COMP CTI	COMPRLM	COMPVFN	COMSMSI	COMS0VU	ZTDPFIL	COMBMCT	COMTLBP	COMKTRN
44	COMCCPM	COMCRDW	COMDGJD	COMP CUA	COMPRLS	COMPVID	COMSMSP	COMS1DS	ZTDP TBD	COMBOVL	COMTM OV	COMKTSA
45	COMCCPT	COMCRSB	COMDSYS	COMP CUT	COMPRNS	COMPVLC	COMSMST	COMS1MV	ZTDP TBS	COMBPFP	COMTMVD	COMKTSC
46	COMCCUA	COMCRSP	COMDTFN	COMP CVI	COMPRSI	COMPVMS	COMSMTR	COMS1RM	ZTDTFIL	COMBPFS	COMTMVP	COMKTST
47	COMCCVI	COMCRTN	COMFCID	COMP C2D	COMPRSS	COMPVPA	COMSMTX	COMS176	ZTDTTAB	COMBRCD	COMTOUT	COMKZFN
48	COMCCVL	COMCSCB	COMFVD2	COMPDDT	COMPSAF	COMPVSP	COMSNCD	COMTBAN	ZTDVERB	COMBSIT	COMTSIT	COMCCDM
49	COMCDCM	COMCSFM	COMFVD3	COMPDLI	COMPSCA	COMPWBB	COMSNET	COMTCVT	ZTDVMT0	COMBSNS	COMTUSE	COMCCDP
50	COMCDCP	COMCSFN	COMFXTI	COMP DTS	COMP SDA	COMPWCB	COMSPDT	COMTDA8	ZTDV PDT	COMBTDM	COMTUSP	COMSSTM
51	COMCDTC	COMCSKW	COMFXVT	COMP DVC	COMP SDI	COMPWEI	COMSPFM	COMTDP6	COMFDS1	COMBUCR	COMTVLD	COMCCKD
52	COMCDXB	COMCSNF	COMFPAN	COMP DV5	COMP SDN	COMPWSS	COMSPFS	COMTDP9	COMFDS2	COMBUDT	COMTVLF	COMCMBS
53	COMCECM	COMCSNM	COMFTIO	COMPECX	COMP SDR	COMPWVE	COMSPFU	COMTDSP	COMFFSE	COMXACM	COMTVLM	COMPTFM
54	COMCECS	COMCSOE	COMFVDT	COMP FAT	COMP SEI	COMSACC	COMSPIM	COMTNAP	COMFMLT	COMXBST	COMTVLP	COMSTFU

COMCEDT	COMCSRI	COMFVD1	COMPFLF	COMPSES	COMSATF	COMSPRD	COMTVDT	COMFONL	COMXCCB	COMTVLV
COMCFCE	COMCSRT	COMPMAC	COMPGBN	COMPSFB	COMSBIO	COMSPRO	COMT6DP	COMFSGL	COMXCTF	COMTVLX
COMCFLD	COMCSSN	COMPACS	COMPGFP	COMPSFE	COMSCIO	COMSQAC	COMT8AD	COMFSMF	COMXEMC	COMKMAC
COMCFQO	COMCSST	COMPANS	COMPGTN	COMPSFI	COMSCPD	COMSQFS	COMT9DP	COMFTAB	COMXEXP	COMKARF
COMCGMS	COMCSTF	COMPAPI	COMPICT	COMPSFN	COMSCPS	COMSREM	COMUCPD	COMFXCM	COMXFCQ	COMKBRD

DECKS WRITTEN ON COMPILE FILE.

CPUMLD

102600B STORAGE USED. 5950 LINES WRITTEN ON COMPILE FILE.

CPUMLD	
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BINARY CONTROL CARDS.

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***	DAYFILE MESSAGES (BATCH MODE).	CPUMLD	33
*	SYSTEM CONTROL POINT MESSAGES (DEADSTART LOAD).	CPUMLD	34
*		CPUMLD	35
*	* BLOCK ORIGIN TABLE OVERFLOW.* - TOO MANY COMMON BLOCKS FOR	CPUMLD	36
*	ASSEMBLED BLOCK ORIGIN TABLE BUFFER SIZE.	CPUMLD	37
*		CPUMLD	38
*	* INVALID LOADER INPUT.* - UNIDENTIFIED LOADER TABLE	CPUMLD	39
*	ENCOUNTERED, PREMATURE EOR ENCOUNTERED, OR FILL OR XFILL	CPUMLD	40
*	TABLE HAS CONDITIONAL RELOCATION BASE DESIGNATOR SPECIFIED	CPUMLD	41
*	IN ITS HEADER WORD.	CPUMLD	42
*		CPUMLD	43
*	* LOADER TABLE OVERFLOW.* - PRFX, TEXT, ENTR, OR XFER	CPUMLD	44
*	LOADER TABLE WAS ENCOUNTERED THAT WAS TOO LARGE FOR	CPUMLD	45
*	ASSEMBLED LOADER TABLE BUFFER SIZE.	CPUMLD	46
*		CPUMLD	47
*	* RELOCATION TABLE OVERFLOW.* - ASSEMBLED FILL OR XFILL	CPUMLD	48
*	TABLE BUFFER SIZE WAS NOT LARGE ENOUGH TO ACCOMODATE ALL	CPUMLD	49
*	ACCUMULATED FILL OR XFILL TABLE RELOCATION ENTRIES.	CPUMLD	50
*	COMMON DECKS.	CPUMLD	52
		CPUMLD	53
0	CTEXT COMCMAC - CPU SYSTEM MACROS.	CPUMLD	54
	QUAL DSL	COMCMAC	1
		CPUMLD	56
0	CTEXT COMSDSL - DEAD START LOAD PARAMETERS.	COMSDSL	1
	QUAL *	CPUMLD	58
*	TABLE LENGTHS.	CPUMLD	60
		CPUMLD	61
60	.BOL EQU /DSL/CPBL MAXIMUM NUMBER OF BLOCKS	CPUMLD	62
10000	.FBL EQU 10000B FILL TABLE LENGTH	283L840	1
20	.TBL EQU 20B LOADER TABLE LENGTH	CPUMLD	64
400	.XFL EQU 400B XFILL TABLE LENGTH	252L678	2
*	MACROS.	CPUMLD	67
		CPUMLD	68
**	BAST - BLOCK ASSOCIATION TABLE MACRO.	CPUMLD	69
*		CPUMLD	70
*	BAST IS USED TO DEFINE BLOCKS WHICH ARE TO BE LOADED OR	CPUMLD	71
*	IGNORED AT DEAD START. THIS MACRO MAY BE USED TO DEFINE TWO	CPUMLD	72
*	ASSOCIATED BLOCKS OF WHICH ONE IS LOADED DURING DEADSTART OR	CPUMLD	73
*	IT MAY BE USED TO DEFINE A SINGLE BLOCK WHICH MAY BE	CPUMLD	74
*	LOADED OR IGNORED AT DEADSTART.	CPUMLD	75
*		CPUMLD	76
*	FORMAT -	CPUMLD	77
*		CPUMLD	78
* TAG	BAST BN,ABN	CPUMLD	79
*		CPUMLD	80

MACROS

* ENTRY BN - BLOCK NAME. (DEFAULT BLOCK TO LOAD)
* ABN - ALTERNATE BLOCK TO LOAD IF PRESENT.

CPUMLD 81
CPUMLD 82
CPUMLD 83
CPUMLD 84
CPUMLD 85
CPUMLD 86
CPUMLD 87
CPUMLD 88
CPUMLD 89

MACRO BAST,TAG,BN,ABN
TAG VFD 42/0L_BN,18/1
IFC NE,*ABN**,1
VFD 42/0L_ABN,18/0
ENDM

1412THE

```

**      INMOD - THIS MACRO IN CONJUNCTION WITH THE FOLLOWING OPDEF,S
*      MAKES IT POSSIBLE TO SPECIFY A MODIFIER ON ALL 30 BIT
*      INCREMENT INSTRUCTIONS.  THIS MODIFER MUST BE IN THE FORM
*      OF A POINTER TO ONE OF THE TABLE POINTERS THAT IS SET
*      DYNAMICALLY AT INITIALIZATION TIME. THE INSTRUCTION WILL
*      BE MODIFIED DURING INITIALIZATION.
*      THIS IS USEFUL FOR ACCESSING THE POINTERS THAT ARE
*      DYNAMICALLY SET AT INITIALIZATION TIME SUCH AS THE ORGIN
*      ADDRESS.
*
*      DEFINITIONS.
*
*      INMOD  PTRA,LWAF
*
*      PTRA = POINTER DESIRED.
*      LWAF = IF DEFINED TAKE LWA INSTEAD OF FWA.
*
*      EXAMPLE-
*      TX6    0,0B
*      THIS WILL GENERATE A 30 BIT INSTRUCTION OF THE FOLLOWING FORM
*      SX6    B0+K
*      WHERE K = THE FIRST WORD ADDRESS FOR CPUMTR RELOCATION.
*
*      INMOD  MACRO  PTRA,LWAF
*             LOCAL INM1,INM2
*      INM1   SET    *P
*      INM2   SET    *
*             IFEQ   INM1,60,2
*      INM1   SET    0
*      INM2   SET    *-1
*      TINST  RMT
*             VFD    12/2000B+INM1
*             VFD    18/PTRA
*             IFC    EQ,*LWAF**
*             VFD    12/0
*             ELSE   1
*             VFD    12/4000B
*             VFD    18/INM2
*      TINST  RMT
*             ENDM
*
*      OPDEF,S USED WITH INMOD.
*
*      TAAQ,Q  OPDEF  P1,P2,P3,P4
*             SA.P1  A.P2+P3
*             INMOD  P4
*             ENDM
*
*      TAA,Q   OPDEF  P1,P2,P4
*             SA.P1  A.P2+0
*             INMOD  P4
```

```

CPUMLD      92
CPUMLD      93
CPUMLD      94
CPUMLD      95
CPUMLD      96
CPUMLD      97
CPUMLD      98
CPUMLD      99
CPUMLD     100
CPUMLD     101
CPUMLD     102
CPUMLD     103
CPUMLD     104
CPUMLD     105
CPUMLD     106
CPUMLD     107
CPUMLD     108
CPUMLD     109
CPUMLD     110
CPUMLD     111
CPUMLD     112
CPUMLD     113
CPUMLD     114
CPUMLD     115
CPUMLD     116
CPUMLD     117
CPUMLD     118
CPUMLD     119
CPUMLD     120
CPUMLD     121
CPUMLD     122
CPUMLD     123
CPUMLD     124
CPUMLD     125
CPUMLD     126
CPUMLD     127
CPUMLD     128
CPUMLD     129
CPUMLD     130
CPUMLD     131
CPUMLD     132
CPUMLD     134
CPUMLD     135
CPUMLD     136
CPUMLD     137
CPUMLD     138
CPUMLD     139
CPUMLD     140
CPUMLD     141
CPUMLD     142
CPUMLD     143
CPUMLD     144
CPUMLD     145
```

ENDM

CPUMLD 146
CPUMLD 147
CPUMLD 148
CPUMLD 149
CPUMLD 150
CPUMLD 151
CPUMLD 152
CPUMLD 153
CPUMLD 154
CPUMLD 155

TAAQ,Q,Q OPDEF P1,P2,P3,P4,P5
SA.P1 A.P2+P3
INMOD P4,P5
ENDM

0

CTEXT REMAINING OPDEF,S USED WITH INMOD.

** TVFD - DEFINE A RELOCATABLE *VFD*.

CPUMLD 401
CPUMLD 402
CPUMLD 403
CPUMLD 404
CPUMLD 405
CPUMLD 406
CPUMLD 407
CPUMLD 408
CPUMLD 409
CPUMLD 410

*
* TVFD N/V,X
*

* ENTRY *N* = BIT COUNT
* *V* = VALUE
* *R* = ADDRESS OF RELOCATION VALUE
* IF *R* IS NOT PRESENT, THE ORIGIN WILL BE USED
* FOR RELOCATION.

TVFD MACRO V,R

* EXTRACT FIELD WIDTH

.1 MICRO 1,,/V
.1 MICCNT .1

* CHECK FOR RELOCATION REQUIRED

.2 MICRO .1+2,, V
IF DEF,".2",3
IF -REL,".2",2
VFD ".1"/".2"

.1 SKIP

* PROCESS RELOCATION

VFD ".1"/".2"-ORG
INMOD OB
.1 ENDIF

IFC NE,\$R\$,1
INMOD R
ENDM

CPUMLD 411
CPUMLD 412
CPUMLD 413
CPUMLD 414
CPUMLD 415
CPUMLD 416
CPUMLD 417
CPUMLD 418
CPUMLD 419
CPUMLD 420
CPUMLD 421
CPUMLD 422
CPUMLD 423
CPUMLD 424
CPUMLD 425
CPUMLD 426
CPUMLD 427
CPUMLD 428

1

1

544

545

546

1

553

555

556

557

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560

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564

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576

577

578

$$\frac{579}{500}$$
580
581581
582

583

584

585

586

* GLOBAL STORAGE.

CPUMLD	588
CPUMLD	589
CPUMLD	590
252L678	4
CPUMLD	592
CPUMLD	593
CPUMLD	594
CPUMLD	595
CPUMLD	596
CPUMLD	597
CPUMLD	598
CPUMLD	599
CPUMLD	600
CPUMLD	601
CPUMLD	602
CPUMLD	603
CPUMLD	604
CPUMLD	605
CPUMLD	606

115	7	.ER	BSS	7	125 +	.RL	EQU	.RP+1
124	00000000000000000000	.RP	CON	0,0	20	.TB	BSS	.TBL
125	00000000000000000000					.BO	VFD	42/0
							TVFD	18/0,0B
126							BSS	.BOL-1
146	0000000000000000						CON	0
	000000						VFD	42/0
147	57						TVFD	18/0,MBUF
226	00000000000000000000	.CP					BSS	.BOL-2
227	0000000000000000						CON	0
	000000							
230	56							
306	00000000000000000000	.LL	CON	0				
307	00000000000000000000	.EP	CON	0				
310	000000000000000000001	.BC	CON	1				
311	10000	.FB	BSS	.FBL				
10311	400	.XF	BSS	.XFL				

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60
BUFFER TO SAVE REGISTERS FOR *CPUMTR*																																																											
NEXT RELOCATABLE WORD POINTER																																																											
CURRENT RELOCATABLE BUFFER LIMIT																																																											
LOADER TABLE BUFFER																																																											
BLOCK ORIGINS																																																											
CURRENT POSITION IN LOAD BUFFER																																																											
TOTAL LOAD LENGTH																																																											
CPUMTR ENTRY POINT ADDRESS																																																											
BATCH MODE FLAG																																																											
FILL TABLE																																																											
XFILL TABLE																																																											

** ABP - ADVANCE BUFFER POINTER.						CPUMLD	609
*						CPUMLD	610
* EXIT *LTPT* TABLE ENTRY BUFFER FWA ADVANCED.						CPUMLD	611
* TO *ERR1* IF HEADER WORD HAS CONDITIONAL RELOCATION						CPUMLD	612
* BASE DESIGNATOR SPECIFIED.						CPUMLD	613
* * * * *						CPUMLD	614
* USES A - 2, 6.						CPUMLD	615
* X - 0, 1, 4, 5, 6.						CPUMLD	616
* * * * *						CPUMLD	617
10711 ABP BSS 0 ENTRY						CPUMLD	618
10711 57231 SA2 B3-B1 ADVANCE BUFFER FWA						CPUMLD	619
43052 MX0 42						CPUMLD	620
76420 SX4 B2 DECREMENT BUFFER LENGTH						CPUMLD	621
76140 SX1 B4						CPUMLD	622
10712 11602 BX6 X0*X2						CPUMLD	623
20422 LX4 18						CPUMLD	624
12661 BX6 X6+X1						CPUMLD	625
20547 LX5 59-20						CPUMLD	626
10713 37664 IX6 X6-X4						CPUMLD	627
21563 AX5 51						CPUMLD	628
5062000000 SA6 A2+						CPUMLD	629
10714 0315011157 + NZ X5,ERR1 IF CONDITIONAL RELOCATION BASE SPECIFIED						CPUMLD	630
0400000006 + EQ CPUR RETURN						CPUMLD	631
* * * * *						CPUMLD	632
* * * * *						CPUMLD	633
** ENT - PROCESS ENTR TABLE.						CPUMLD	634
*						CPUMLD	635
* EXIT (.EP)= ADDRESS OF CPUMTR ENTRY.						CPUMLD	636
* * * * *						CPUMLD	637
* USES A - 1, 6.						CPUMLD	638
* X - 0, 1, 2, 6, 7.						CPUMLD	639
* * * * *						CPUMLD	640
10715 ENT BSS 0 ENTRY						CPUMLD	641
10715 43752 MX7 -18						CPUMLD	642
5111000126 + SA1 .TB+B1 READ ENTRY LOCATION						CPUMLD	643
73210 SX2 X1 RELATIVE ADDRESS						CPUMLD	644
10716 20152 LX1 0-18 EXTRACT BLOCK NUMBER						CPUMLD	645
43063 MX0 -9						CPUMLD	646
15610 BX6 -X0*X1						CPUMLD	647
10717 7206777774 SX0 X6-3						CPUMLD	648
5110000146 + SA1 .B0 ASSUME PROGRAM BLOCK RELOCATION						CPUMLD	649
10720 0330010721 + NG X0,ENT1 IF PROGRAM BLOCK						CPUMLD	650
5216000146 + SA1 .B0+X6 READ COMMON BLOCK ORIGIN ADDRESS						CPUMLD	651
10721 15717 ENT1 BX7 -X7*X1 RELOCATE ENTRY						CPUMLD	652
36672 IX6 X7+X2						CPUMLD	653
5160000307 + SA6 .EP						CPUMLD	654
10722 0200000006 + JP CPUR RETURN						CPUMLD	655
* * * * *						CPUMLD	656
* * * * *						CPUMLD	657
* * * * *						CPUMLD	658
* * * * *						CPUMLD	659
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* * * * *						CPUMLD	670
* * * * *						CPUMLD	671
* * * * *						CPUMLD	672
* * * * *						CPUMLD	673
* * * * *						CPUMLD	674
* * * * *						CPUMLD	675
* * * * *						CPUMLD	676
* * * * *						CPUMLD	677
* * * * *						CPUMLD	678
* * * * *						CPUMLD	679
* * * * *						CPUMLD	680

**	FIL - PROCESS FILL TABLE.					CPUMLD	658
*						CPUMLD	659
*	ENTRY (.FB) = ACCUMULATED FILL TABLE DATA.					CPUMLD	660
*	(FBAE) = LWA+1 DATA IN FILL TABLE.					CPUMLD	661
*						CPUMLD	662
*	USES A - 1, 3, 5, 6, 7.					CPUMLD	663
*	B - 2, 3, 5, 6, 7.					CPUMLD	664
*	X - ALL.					CPUMLD	665
						CPUMLD	666
						CPUMLD	667
10723	00000000000000000000	FIL	SUBR	ENTRY/EXIT		CPUMLD	668
10724	5110000104 +		SA1	FBAE	GET LWA+1 DATA IN FILL TABLE	CPUMLD	669
	43402		MX4	2	CONTROL BIT MASK	CPUMLD	670
	63210		SB2	X1		CPUMLD	671
10725	66511		SB5	B1+B1	SET RELOCATABLE ENTRIES PER WORD COUNT	CPUMLD	672
	20435		LX4	2+27		CPUMLD	673
	5150000311 +		SA5	.FB	FIRST WORD IN FILL TABLE	CPUMLD	674
						CPUMLD	675
		*	RELOCATE ADDRESSES.			CPUMLD	676
						CPUMLD	677
10726	43063	FIL1	MX0	-9		CPUMLD	678
	0325010740 +		PL	X5,FIL4	IF CONTROL BYTE	CPUMLD	679
	20514		LX5	12	EXTRACT RELOCATION INDEX	CPUMLD	680
10727	15250		BX2	-X0*X5		CPUMLD	681
	5232000226 +		SA3	.CP+X2	BLOCK WHICH CONTAINS RELOCATION WORD	CPUMLD	682
	20522		LX5	18		CPUMLD	683
10730	63630		SB6	X3		CPUMLD	684
	0460010735 +		ZR	B6,FIL2	IF RELOCATING IN A NON-LOADED BLOCK	CPUMLD	685
	11245		BX2	X4*X5	EXTRACT CONTROL BITS	CPUMLD	686
10731	20241		LX2	-27		CPUMLD	687
	53356		SA3	X5+B6	WORD TO BE RELOCATED	CPUMLD	688
	5222010742 +		SA2	X2+FILA	SHIFT COUNT FOR RELOCATION POSITION	CPUMLD	689
10732	10611		BX6	X1		CPUMLD	690
	43752		MX7	-18		CPUMLD	691
	63320		SB3	X2	SHIFT COUNT FOR RELOCATION POSITION	CPUMLD	692
	20736		LX7	30		CPUMLD	693
10733	23636		AX6	X6,B3		CPUMLD	694
	23737		AX7	X7,B3		CPUMLD	695
	12273		BX2	X7+X3	EXTRACT FIELD TO RELOCATE	CPUMLD	696
	36626		IX6	X2+X6	ADD RELOCATION VALUE	CPUMLD	697
10734	11373		BX3	X7*X3	CLEAR FIELD IN WORD	CPUMLD	698
	15667		BX6	-X7*X6	EXTRACT RELOCATED FIELD	CPUMLD	699
	12636		BX6	X3+X6	MERGE RELOCATED FIELD	CPUMLD	700
	54630		SA6	A3		CPUMLD	701
10735	67551	FIL2	SB5	B5-B1		CPUMLD	702
	0550010726 +		NZ	B5,FIL1	IF MORE RELOCATABLE ENTRIES IN WORD	CPUMLD	703
	66511		SB5	B1+B1	RESET RELOCATABLE ENTRIES PER WORD COUNT	CPUMLD	704
10736	64751		SB7	A5+B1		CPUMLD	705
	0672010723 +		GE	B7,B2,FILX	IF END OF FILL TABLE	CPUMLD	706
	56570		SA5	B7		CPUMLD	707
10737	0400010726 +		EQ	FIL1	PROCESS NEXT RELOCATION ENTRY	CPUMLD	708
						CPUMLD	709
		*	REPLACE BLOCK INDEX.			CPUMLD	710
						CPUMLD	711
10740	20536	FIL4	LX5	30	SET BLOCK ORIGIN	CPUMLD	712
	15250		BX2	-X0*X5		CPUMLD	713
	5212000146 +		SA1	.B0+X2		CPUMLD	714

10741 73110
20136
0400010735 +

SX1 X1
LX1 30
EQ FIL2

CPUMLD 715
CPUMLD 716
CPUMLD 717
CPUMLD 718
CPUMLD 719
CPUMLD 720
CPUMLD 721
CPUMLD 722
CPUMLD 723
CPUMLD 724
CPUMLD 725
CPUMLD 726

* SHIFT COUNTS TO POSITION RELOCATION VALUE.

10742
10742 00000000000000000036
10743 00000000000000000017
10744 00000000000000000000
10745 00000000000000000060

FILA BSS 0
CON 30 LOWER
CON 15 MIDDLE
CON 0 UPPER
CON 48 UNDEFINED (SHIFT END OFF)

** PID - PROCESS PIDL TABLE.

*

ENTRY (B2) = TABLE WORD COUNT.

*

EXIT (.B0) = BLOCK ORIGINS.

*

(.LL) = TOTAL LOAD LENGTH.

*

USES B - 2, 3.

*

A - 1, 2, 3, 4, 6, 7.

*

X - 0, 1, 2, 3, 4, 6, 7.

*

CALLS SBT.

10746
10746 7160000000
5110000150 +
10747 10066
5160000146 +
14700

PID BSS 0 ENTRY
TX6 0,0B PRESET PROGRAM BLOCK ORIGIN
SA1 .B0+2 ASSIGN BLOCK ORIGINS

10750 73110
54661
36061

SX1 X1
SA6 A6+B1
IX0 X6+X1 ORIGIN OF NEXT BLOCK

10751 5011000001
67221
10700

PID1 SA7 A6+B1
SA1 A1+1
SB2 B2-B1 DECREMENT BLOCK COUNT

10752 0420010773 +

BX7 X0
ZR B2,PID4 IF END OF BLOCKS

* CHECK IF BLOCK TO BE LOADED.

0100011251 +

RJ SBT

10753 73440
43752
73210

SX4 X4
MX7 42
SX2 X1 LOAD LENGTH OF THIS BLOCK

10754 0304010772 +
7064766766 -

BX1 X7*X1 BLOCK NAME
ZR X4,PID3 IF BLOCK NOT TO BE LOADED
SX6 A4-SPBOL

10755 0326010764 +
74610

PL X6,PID2 IF NOT SPECIAL ORIGIN BLOCK
SX6 A1 SAVE POINTER

10756 7034766774 -

SX3 A4-PIDA

10757	5263011056 +	7074766772 -	SX7	A4-SBDR		CPUMLD	769
			SA6	SPBP+X3		CPUMLD	770
		0327010751 +	PL	X7,PID1	IF BLOCK RELOCATION DEFERRED	CPUMLD	771
10760	5130000310 +		SA3	.BC		CPUMLD	772
		0303010764 +	ZR	X3,PID2	IF BATCH MODE	CPUMLD	773
10761	5140011055 +		SA4	SPB0	GET SPECIAL BLOCK ORIGIN ADDRESS	CPUMLD	774
		10744	BX7	X4		CPUMLD	775
		36642	IX6	X4+X2	ADVANCE SPECIAL BLOCK ORIGIN ADDRESS	CPUMLD	776
10762	5071000060		SA7	A1-.B0+.CP	SET LOAD BUFFER ADDRESS	CPUMLD	777
		54640	SA6	A4		CPUMLD	778
10763	0400010772 +		EQ	PID3	SET BLOCK ORIGIN ADDRESS	CPUMLD	779
						CPUMLD	780
10764	10400	PID2	BX4	X0		CPUMLD	781
	36002		IX0	X0+X2	ADVANCE ORIGIN ADDRESS	CPUMLD	782
		7274000000	TX7	X4,-OB		CPUMLD	783
10765	7277000000		TX7	X7,MBUF		CPUMLD	784
		5071000060	SA7	A1-.B0+.CP	RELOCATED ORIGIN ADDRESS	CPUMLD	785
10766	7064766742 -		SX6	A4-ECS		252L678	8
		0316010772 +	NZ	X6,PID3	IF NOT *EM* BLOCK	252L678	9
						252L678	10
		*		REDUCE EM LWA.		252L678	11
						252L678	12
10767	5120000077		SA2	MEFL		252L678	13
		7200777377	SX0	X0-400B		252L678	14
10770	76611		SX6	B1+B1		252L678	15
	43772		MX7	-2		252L678	16
		20237	LX2	-29		252L678	17
		15227	BX2	-X7*X2		252L678	18
10771	6232000004		SB3	X2+4		252L678	19
		22636	LX6	B3		252L678	20
		36006	IX0	X0+X6	LWA = LWA - (2*200B) + (2*SECTOR LIMIT)	252L678	21
10772	12641	PID3	BX6	X4+X1	BLOCK NAME + ORIGIN ADDRESS	CPUMLD	786
	54610		SA6	A1	SET BLOCK ORIGIN ADDRESS	CPUMLD	787
		0400010751 +	EQ	PID1	PROCESS NEXT BLOCK	CPUMLD	788
						CPUMLD	789
10773	7100000000	PID4	TX0	0,PBUF	LOAD ADDRESS FOR PRESET CODE	CPUMLD	790
		6120000002	SB2	SBDR-PIDA		CPUMLD	791
10774	43352		MX3	42		CPUMLD	792
	10700		BX7	X0		CPUMLD	793
10775	5122011056 +	PID5	SA2	SPBP+B2	GET POINTER	CPUMLD	794
		66221	SB2	B2+B1		CPUMLD	795
		53120	SA1	X2		CPUMLD	796
10776	7142777771		SX4	B2-SPBPL		CPUMLD	797
		73210	SX2	X1		CPUMLD	798
		11131	BX1	X3*X1	BLOCK NAME	CPUMLD	799
10777	12610		BX6	X1+X0		CPUMLD	800
	36002		IX0	X0+X2		CPUMLD	801
		5071000060	SA7	A1-.B0+.CP	RELOCATED ORIGIN ADDRESS	CPUMLD	802
11000	36772		IX7	X7+X2		CPUMLD	803
	54610		SA6	A1	SET BLOCK ORIGIN ADDRESS	CPUMLD	804
		0334010775 +	NG	X4,PID5	IF MORE RELOCATIONS	CPUMLD	805
11001	73700		SX7	X0	LWA+1 OF LOAD	CPUMLD	806
		5170000306 +	SA7	.LL		CPUMLD	807
11002	0400000006 +		EQ	CPUR	RETURN	CPUMLD	808
						CPUMLD	809
		*		TABLE OF KNOWN CPUMTR BLOCKS.		CPUMLD	810
						CPUMLD	811

11003		PIDA	BSS	0		CPUMLD	812
						CPUMLD	813
11003	30203442410000000001	XP176	BAST	XP176		CPUMLD	814
11004	30204334440000000001	XP819	BAST	XP819		CPUMLD	815
	11005 +	SBDR	EQU	*	START OF BLOCKS WITH DEFERRED RELOCATION	CPUMLD	816
11005	20220523052400000001	PRESET	BAST	PRESET		CPUMLD	817
11006	24220514000000000001	TREL	BAST	TREL	RELOCATION DATA BLOCK	CPUMLD	818
11007	24121520000000000001	TJMP	BAST	TJMP		CPUMLD	819
11010	24021413000000000001	TBLK	BAST	TBLK	LOADED BLOCKS NAME TABLE BLOCK	CPUMLD	820
	11011 +	SPBOL	EQU	*	SPECIAL ORIGIN BLOCKS LIMIT	CPUMLD	821
11011	03150500000000000001	CME	BAST	CME,0CME		CPUMLD	822
11013	03152500000000000001	CMU	BAST	CMU,0CMU		CPUMLD	823
11015	03152515242200000001	CMUMTR	BAST	CMUMTR,0CMUMTR		CPUMLD	824
11017	03202000000000000001	CPP	BAST	CPP		251L664	1
11020	20032000000000000001	PCP	BAST	PCP		271L716	1
11021	04032000000000000001	DCP	BAST	DCP		CPUMLD	825
11022	16260500000000000001	NVE	BAST	NVE		NS21000	3
11023	23250203200000000001	SUBCP	BAST	SUBCP		CPUMLD	826
11024	11230415150600000001	ISDMMF	BAST	ISDMMF		CPUMLD	827
11025	15150600000000000001	MMF	BAST	MMF		251L664	2
11026	14232015150600000001	LSPMMF	BAST	LSPMMF		251L664	3
11027	02111715150600000001	BIOMMF	BAST	BIOMMF		283L840	2
11030	11230400000000000001	ISD	BAST	ISD		CPUMLD	829
11031	26152300000000000001	VMS	BAST	VMS		CPUMLD	830
11032	23032000000000000001	SCP	BAST	SCP		CPUMLD	831
11033	23032025050300000001	SCPUEC	BAST	SCPUEC		CPUMLD	832
11034	03230500000000000001	CSE	BAST	CSE		NS21000	4
11035	05032300000000000001	ECS	BAST	ECS		CPUMLD	833
11036	05032302250600000001	ECSBUF	BAST	ECSBUF		CPUMLD	834
11037	05050300000000000001	EEC	BAST	EEC		NS21000	5
11040	05250500000000000001	EUE	BAST	EUE		NS21000	6
11041	23250500000000000001	SUE	BAST	SUE		NS21000	7
11042	15150602250600000001	MMFBUF	BAST	MMFBUF		CPUMLD	835
11043	03204336330000000001	CP830	BAST	CP830		NS2332	1
11044	03203442410000000001	CP176	BAST	CP176		CPUMLD	836
11045	11104334440000000001	IH819	BAST	IH819		CPUMLD	837
11046	11102006150400000001	IHPFMD	BAST	IHPFMD		NS21000	8
11047	02250611170000000001	BUFIO	BAST	BUFIO		NS21000	9
11050	20221702050000000001	PROBE	BAST	PROBE		CPUMLD	840
11051	24220103050000000001	TRACE	BAST	TRACE		271L716	2
11052	15030500000000000001	MCE	BAST	MCE		NS22000	1
11053	25050300000000000001	UEC	BAST	UEC		CPUMLD	841
						CPUMLD	842
11054	00000000000000000001		CON	1	DEFAULT LOAD ANY *UNKNOWN* BLOCK	CPUMLD	843
						CPUMLD	844
	52	PIDAL	EQU	*-PIDA		CPUMLD	845
						CPUMLD	846
11055	00000000000000000000	SPBO	CON	0	SPECIAL BLOCKS ORIGIN	CPUMLD	847
						CPUMLD	848
11056	6	SPBP	BSS	SPBOL-PIDA	SPECIAL BLOCK POINTERS	CPUMLD	849
	6	SPBPL	EQU	*-SPBP		CPUMLD	850

1412THE

** TXT - PROCESS TEXT TABLE.

CPUMLD 852

*

CPUMLD 853

*

ENTRY (B2) = TABLE WORD COUNT.

CPUMLD 854

*

(X5) = TEXT TABLE CONTROL WORD.

CPUMLD 855

*

CPUMLD 856

*

USES A - 1, 2, 3, 5, 7.

CPUMLD 857

*

B - 3, 4, 6, 7.

CPUMLD 858

*

X - ALL.

CPUMLD 859

CPUMLD 860

CPUMLD 861

11064

TXT

BSS

0

ENTRY

CPUMLD 862

CPUMLD 863

*

INITIALIZE TEXT RELOCATION LOOP.

CPUMLD 864

CPUMLD 865

11064 63650

SB6

X5

RELATIVE ADDRESS IN BLOCK

CPUMLD 866

67721

SB7

B2-B1

(B7) = WORD COUNT

CPUMLD 867

20552

LX5

-18

CPUMLD 868

63450

SB4

X5

CPUMLD 869

11065 5225000226 +

SA2

X5+.CP

CURRENT POSITION IN LOAD BUFFER

CPUMLD 870

0302000006 +

ZR

X2,CPUR

IF NOT LOADING THIS BLOCK

CPUMLD 871

11066 6130011120 +

SB3

TXTB

(B3) = ADDRESS MASKS

CPUMLD 872

5110000126 +

SA1

.TB

(X1) = CONTROL WORD

CPUMLD 873

11067 63626

SB6

B6+X2

CPUMLD 874

43070

MX0

-4

BYTE MASK

CPUMLD 875

57361

SA3

B6-B1

CPUMLD 876

20104

LX1

4

SHIFT TO FIRST BYTE

CPUMLD 877

11070 15410

BX4

-X0*X1

FIRST CONTROL BYTE

CPUMLD 878

54511

SA5

A1+B1

FIRST TEXT WORD

CPUMLD 879

10733

BX7

X3

CPUMLD 880

53243

SA2

B3+X4

FIRST ADDRESS MASK WORD

CPUMLD 881

11071 20104

LX1

4

SHIFT TO NEXT CONTROL BYTE

CPUMLD 882

5042777756

SA4

A2+TXTA-TXTB

FIRST RELOCATION WORD

CPUMLD 883

54730

SA7

A3

CPUMLD 884

CPUMLD 885

*

TEXT RELOCATION LOOP.

CPUMLD 886

*

THE FOLLOWING PROCEDURE RELOCATES ADDRESSES IN ONES-S

CPUMLD 887

*

COMPLEMENT ARITHMETIC FOR ALL ADDRESS FIELDS IN TEXT WORDS.

CPUMLD 888

*

(1.) COMPLEMENT ADDRESSES.

CPUMLD 889

*

(2.) ADD TO RELOCATE.

CPUMLD 890

*

(3.) COMPLEMENT RESULT.

CPUMLD 891

*

(4.) MERGE RELOCATED ADDRESSES WITH TEXT WORD.

CPUMLD 892

CPUMLD 893

11072 15725

TXT2

BX7

-X5*X2

EXTRACT COMPLEMENT ADDRESS FIELDS

CPUMLD 894

15652

BX6

-X2*X5

REMOVE ADDRESSES FROM TEXT

CPUMLD 895

36774

IX7

X7+X4

ADD COMPLEMENT ADDRESSES

CPUMLD 896

15510

BX5

-X0*X1

NEXT RELOCATION BYTE

CPUMLD 897

11073 15372

BX3

-X2*X7

EXTRACT CARRYS

CPUMLD 898

15727

BX7

-X7*X2

MASK AND COMPLIMENT ADDRESSES

CPUMLD 899

21322

AX3

18

CPUMLD 900

53253

SA2

X5+B3

NEXT ADDRESS MASK WORD

CPUMLD 901

11074 54551

SA5

A5+B1

NEXT TEXT WORD

CPUMLD 902

5042777756

SA4

A2+TXTA-TXTB

NEXT RELOCATION WORD

CPUMLD 903

37773

IX7

X7-X3

ADD CARRYS

CPUMLD 904

11075 20104

LX1

4

SHIFT CONTROL WORD

CPUMLD 905

12776

BX7

X7+X6

INSERT RELOCATED ADDRESSES

CPUMLD 906

67771

SB7

B7-B1

DECREMENT WORD COUNT

CPUMLD 907

54771

SA7

A7+B1

STORE PREVIOUS WORD

CPUMLD 908

11076 0570011072 +
02000000006 +

NZ
JP

B7,TXT2
CPUR

LOOP FOR ALL TEXT WORDS
RETURN

CPUMLD
CPUMLD

909
910

** TXTA - TABLE OF COMPLEMENT RELOCATION ADDRESSES.
* INDEXED BY RELOCATION CONTROL BYTES.
* GENERATED BY *PIDL* PROCESSOR.

CPUMLD 912
CPUMLD 913
CPUMLD 914
CPUMLD 915
CPUMLD 916
CPUMLD 917
CPUMLD 918
CPUMLD 919
CPUMLD 920
CPUMLD 921
CPUMLD 922
CPUMLD 923
CPUMLD 924
CPUMLD 925
CPUMLD 926
CPUMLD 927
CPUMLD 928
CPUMLD 929
CPUMLD 930
CPUMLD 931
CPUMLD 932
CPUMLD 933
CPUMLD 934
CPUMLD 935
CPUMLD 936
CPUMLD 937
CPUMLD 938
CPUMLD 939
CPUMLD 940
CPUMLD 941
CPUMLD 942
CPUMLD 943
CPUMLD 944
CPUMLD 945
CPUMLD 946
CPUMLD 947
CPUMLD 948
CPUMLD 949
CPUMLD 950
CPUMLD 951
CPUMLD 952
CPUMLD 953
CPUMLD 954
CPUMLD 955
CPUMLD 956
CPUMLD 957
CPUMLD 958
CPUMLD 959
CPUMLD 960
CPUMLD 961
CPUMLD 962

11077 TXTA BSS 0

11077 00000000000000000000 VFD 60/0

11100 00000000000000000000 VFD 60/0

11101 0000000000000000 VFD 42/0

000000 TVFD 18/0,-OB

11102 0000000000000000 VFD 42/0

000000 TVFD 18/0,OB

11103 0000000000 VFD 27/0

000000 TVFD 18/0,-OB

000000 VFD 15/0

11104 0000000000 VFD 27/0

000000 TVFD 18/0,-OB

000000 VFD 15/0

11105 0000000000 VFD 27/0

000000 TVFD 18/0,OB

000000 VFD 15/0

11106 0000000000 VFD 27/0

000000 TVFD 18/0,OB

000000 VFD 15/0

11107 0000 VFD 12/0

000000 TVFD 18/0,-OB

0000000000 VFD 30/0

11110 0000 VFD 12/0

000000 TVFD 18/0,-OB

0000000000 VFD 30/0

11111 0000 VFD 12/0

000000 TVFD 18/0,-OB

0000 VFD 12/0

000000 TVFD 18/0,-OB

11112 0000 VFD 12/0

000000 TVFD 18/0,-OB

0000 VFD 12/0

000000 TVFD 18/0,OB

11113 0000 VFD 12/0

000000 TVFD 18/0,OB

0000000000 VFD 30/0

11114 0000 VFD 12/0

000000 TVFD 18/0,OB

0000000000 VFD 30/0

11115 0000 VFD 12/0

000000 TVFD 18/0,OB

0000 VFD 12/0

1412THE

1

0627011140 +		GE	B2,B7,VFDX	IF END OF XFILL TABLE	CPUMLD	1014
56520		SA5	B2	GET NEXT RELOCATION ENTRY	CPUMLD	1015
11144	43463	MX4	-9		CPUMLD	1016
	15154	BX1	-X4*X5	BLOCK RELOCATION INDEX	CPUMLD	1017
	5211000226 +	SA1	X1+.CP		CPUMLD	1018
11145	21511	AX5	9		CPUMLD	1019
	15254	BX2	-X4*X5		CPUMLD	1020
	5222000146 +	SA2	X2+.B0	BLOCK WHICH CONTAINS RELOCATION WORD	CPUMLD	1021
11146	73220	SX2	X2		CPUMLD	1022
	21511	AX5	9		CPUMLD	1023
	43466	MX4	-6		CPUMLD	1024
	63610	SB6	X1		CPUMLD	1025
11147	0460011143 +	ZR	B6,VFD1	IF BLOCK NOT LOADED	CPUMLD	1026
	15154	BX1	-X4*X5	SIZE OF FIELD	CPUMLD	1027
	21506	AX5	6		CPUMLD	1028
11150	15454	BX4	-X4*X5	POSITION OF FIELD	CPUMLD	1029
	21506	AX5	6		CPUMLD	1030
	6221777776	SB2	X1-1		CPUMLD	1031
11151	43701	MX7	1	FORM FIELD MASK	CPUMLD	1032
	23727	AX7	X7,B2		CPUMLD	1033
	63340	SB3	X4		CPUMLD	1034
	63213	SB2	X1+B3		CPUMLD	1035
11152	22727	LX7	X7,B2		CPUMLD	1036
	53156	SA1	X5+B6	READ WORD TO RELOCATE	CPUMLD	1037
	22232	LX2	X2,B3	POSITION RELOCATION	CPUMLD	1038
	16617	BX6	-X7+X1	RELOCATE FIELD	CPUMLD	1039
11153	36662	IX6	X6+X2		CPUMLD	1040
	15117	BX1	-X7*X1		CPUMLD	1041
	11676	BX6	X7*X6		CPUMLD	1042
	12661	BX6	X6+X1		CPUMLD	1043
11154	54610	SA6	A1	STORE RELOCATED WORD	CPUMLD	1044
	0400011143 +	EQ	VFD1	CONTINUE PROCESSING TABLE	CPUMLD	1045
		**	XFR - PROCESS TRANSFER TABLE.			CPUMLD 1047
		*	THE TRANSFER ADDRESS IS IGNORED, THE ADDRESS FROM THE			CPUMLD 1048
		*	*ENTR* TABLE IS USED FOR THE ENTRY POINT ADDRESS.			CPUMLD 1049
					CPUMLD	1050
					CPUMLD	1051
11155		XFR	BSS	0	CPUMLD	1052
11155	0200000006 +	JP	CPUR	ENTRY RETURN	CPUMLD	1053

** ERR - PROCESS LOADER TABLE ERROR.
*
* ENTRY (X3) = ERROR CODE.

CPUMLD 1056
CPUMLD 1057
CPUMLD 1058

11156	00000000000000000000	ERR	SUBR	ENTRY/EXIT	CPUMLD 1059
11157	5120000310 +	ERR1	SA2 .BC		CPUMLD 1060
	0312011165 +		NZ X2,ERR2	IF NOT BATCH MODE	CPUMLD 1061
11160	73130		MESSAGE X3,,R	ISSUE DIAGNOSTIC MESSAGE	CPUMLD 1062
11162	5120011156 +		SA2 ERR		CPUMLD 1063
	13666		BX6 X6-X6		CPUMLD 1064
	54620		SA6 A2		CPUMLD 1065
11163	0312011156 +		NZ X2,ERRX	IF RETURN DESIRED	CPUMLD 1066
	7160247021		ENDRUN		CPUMLD 1067
11165	5110000070	ERR2	R= A1,NCPL	SET MESSAGE IN SYSTEM CONTROL POINT	CPUMLD 1068
	43060		MX0 -12		CPUMLD 1069
	20130		LX1 -36		CPUMLD 1070
11166	15110		BX1 -X0*X1	NUMBER OF CONTROL POINTS	CPUMLD 1071
	20107		LX1 7		CPUMLD 1072
	53330		SA3 X3		CPUMLD 1073
	54431		SA4 A3+B1		CPUMLD 1074
11167	10633		BX6 X3		CPUMLD 1075
	22704		LX7 X4		CPUMLD 1076
	5261000231		SA6 X1+MS1W+200B		CPUMLD 1077
11170	54341		SA3 A4+B1		CPUMLD 1078
	54761		SA7 A6+B1		CPUMLD 1079
	10633		BX6 X3		CPUMLD 1080
	54431		SA4 A3+B1		CPUMLD 1081
11171	54671		SA6 A7+B1		CPUMLD 1082
11172	10744	ERR3	BX7 X4		CPUMLD 1083
	54761		SA7 A6+B1		CPUMLD 1084
	0400011172 +		EQ ERR3	BLANK THE TOOBS	CPUMLD 1085
11173	55111626011411045514	ERRA	DATA C* INVALID LOADER INPUT.*		CPUMLD 1086
11176	55021417031355172211	ERRB	DATA C* BLOCK ORIGIN TABLE OVERFLOW.*		CPUMLD 1087
11202	55220514170301241117	ERRC	DATA C* RELOCATION TABLE OVERFLOW.*		CPUMLD 1088
11205	55141701040522552401	ERRD	DATA C* LOADER TABLE OVERFLOW.*		CPUMLD 1089

** ROW - READ ONE WORD FROM RELOCATABLE BUFFER.
*
* ENTRY (.RP) = ADDRESS OF NEXT BUFFER WORD.
*
* EXIT (X6) = WORD.
* (.RP) = UPDATED.
* (X1) = 0 IF EOR.
*
* USES A - 2, 4, 6, 7.
* X - 2, 4, 6, 7.
*

CPUMLD 1099
CPUMLD 1100
CPUMLD 1101
CPUMLD 1102
CPUMLD 1103
CPUMLD 1104
CPUMLD 1105
CPUMLD 1106
CPUMLD 1107
CPUMLD 1108
CPUMLD 1109

11230	36771	15121		BX1	-X1*X2	WORD COUNT TRANSFERRED TO BUFFER	CPUMLD	1164	
				IX7	X7+X1	SET BUFFER LIMIT	CPUMLD	1165	
		13674		BX6	X7-X4		CPUMLD	1166	
		5170000125	+	SA7	.RL		CPUMLD	1167	
11231	0306011221		+	ZR	X6,RRBX	IF BLOCK SIZE DID NOT CHANGE	CPUMLD	1168	
		13666		BX6	X6-X6	SET EOR FLAG	CPUMLD	1169	
		54640		SA6	A4		CPUMLD	1170	
11232	0400011221		+	EQ	RRBX	RETURN	CPUMLD	1171	
							CPUMLD	1172	
11233	00000000000000000001			RRBA	CON	1	EOR INDICATOR	CPUMLD	1173
11234	000000000000000011307	+		RRBB	VFD	42/0,18/RBUF	CPUMLD	1174	
			**	RTB - READ ONE TABLE.				CPUMLD	1176
			*					CPUMLD	1177
			*	ENTRY (B3) = LOADER TABLE PROCESSOR TABLE ADDRESS.				CPUMLD	1178
			*					CPUMLD	1179
			*	EXIT TABLE IN .BO, .FB, .TB, OR .XF BUFFER, AS DEFINED IN				CPUMLD	1180
			*	*LTPT* TABLE ENTRY.				CPUMLD	1181
			*	(X1) = 0, IF END OF RECORD ENCOUNTERED.				CPUMLD	1182
			*	(X3) = *ERRA* ERROR MESSAGE ADDRESS.				CPUMLD	1183
			*	(X5) = TABLE HEADER WORD.				CPUMLD	1184
			*	(B2) = TABLE WORD COUNT.				CPUMLD	1185
			*	(B3) = ADDRESS OF ENTRY IN *LTPT* TABLE + 2.				CPUMLD	1186
			*	(B4) = LWA+1 LOADER TABLE IN BUFFER.				CPUMLD	1187
			*	(B6) = PROCESSOR ADDRESS.				CPUMLD	1188
			*	TO *ERR1* IF UNIDENTIFIED LOADER TABLE OR PREMATURE				CPUMLD	1189
			*	EOR ENCOUNTERED, OR IF BUFFER OVERFLOWED.				CPUMLD	1190
			*					CPUMLD	1191
			*	USES B - 2, 3, 4, 5, 6.				CPUMLD	1192
			*	A - 1, 2, 6.				CPUMLD	1193
			*	X - 0, 1, 2, 3, 5, 6, 7.				CPUMLD	1194
			*					CPUMLD	1195
			*	CALLS ROW.				CPUMLD	1196
								CPUMLD	1197
								CPUMLD	1198
11235	00000000000000000000		RTB	SUBR		ENTRY/EXIT	CPUMLD	1199	
11236	7130011173	+		SX3	ERRA	* INVALID LOADER INPUT.*	CPUMLD	1200	
		0100011210	+	RJ	ROW	READ TABLE HEADER	CPUMLD	1201	
11237	0301011235	+		ZR	X1,RTBX	IF EOR ENCOUNTERED	CPUMLD	1202	
		10566		BX5	X6		CPUMLD	1203	
		43014		MX0	12		CPUMLD	1204	
11240	56230		RTB1	SA2	B3	IDENTIFY LOADER TABLE	CPUMLD	1205	
		0302011157	+	ZR	X2,ERR1	IF LOADER TABLE NOT VALID	CPUMLD	1206	
		13652		BX6	X5-X2		CPUMLD	1207	
11241	54121			SA1	A2+B1		CPUMLD	1208	
		11606		BX6	X0*X6		CPUMLD	1209	
		63620		SB6	X2	PROCESSOR ADDRESS	CPUMLD	1210	
		64311		SB3	A1+B1		CPUMLD	1211	
11242	0316011240	+		NZ	X6,RTB1	IF NO MATCH	CPUMLD	1212	
		63410		SB4	X1	FWA BUFFER	CPUMLD	1213	
		20060		LX0	-12		CPUMLD	1214	
11243	21122			AX1	18		CPUMLD	1215	
		11705		BX7	X0*X5	LOADER TABLE WORD COUNT	CPUMLD	1216	
		63510		SB5	X1	BUFFER LENGTH	CPUMLD	1217	

11244	63270	20730		LX7	-36		CPUMLD	1218
				SB2	X7	WORD COUNT OF TABLE	CPUMLD	1219
		0752011250 +		GT	B2,B5,RTB3	IF LOADER TABLE OVERFLOWS BUFFER	CPUMLD	1220
		66542		SB5	B4+B2	LWA+1 LOADER TABLE IN BUFFER	CPUMLD	1221
11245	0100011210 +		RTB2	RJ	ROW	READ TABLE	CPUMLD	1222
11246	0301011157 +			ZR	X1,ERR1	IF PREMATURE EOR	CPUMLD	1223
		56640		SA6	B4		CPUMLD	1224
		66441		SB4	B4+B1		CPUMLD	1225
11247	0745011245 +			LT	B4,B5,RTB2	IF NOT END OF TABLE	CPUMLD	1226
		0400011235 +		EQ	RTBX	RETURN	CPUMLD	1227
							CPUMLD	1228
11250	21122		RTB3	AX1	18	SET ERROR MESSAGE ADDRESS	CPUMLD	1229
		36331		IX3	X3+X1		CPUMLD	1230
		0400011157 +		EQ	ERR1	PROCESS ERROR	CPUMLD	1231
		**		SBT - SEARCH KNOWN BLOCK TABLE.			CPUMLD	1233
		*					CPUMLD	1234
		*		ENTRY (X1) = BLOCK NAME TO SEARCH FOR.			CPUMLD	1235
		*					CPUMLD	1236
		*		EXIT (X4) = TABLE ENTRY OF MATCHED BLOCK NAME.			CPUMLD	1237
		*		= LAST WORD OF TABLE IF NO MATCH FOUND.			CPUMLD	1238
		*		(X6) .GE. 0 IF END OF TABLE REACHED.			CPUMLD	1239
		*		(A4) = ADDRESS OF MATCHED ENTRY OR LAST WORD OF TABLE.			CPUMLD	1240
		*					CPUMLD	1241
		*		USES	A - 4.		CPUMLD	1242
		*			X - 3, 4, 6, 7.		CPUMLD	1243
							CPUMLD	1244
							CPUMLD	1245
11251	00000000000000000000		SBT	SUBR		ENTRY/EXIT	CPUMLD	1246
11252	43352			MX3	42		CPUMLD	1247
		5140011002 +		SA4	PIDA-1	BLOCK TABLE ADDRESS - 1	CPUMLD	1248
		43652		MX6	PIDAL		CPUMLD	1249
11253	54441		SBT1	SA4	A4+B1	NEXT TABLE ENTRY	CPUMLD	1250
		20601		LX6	1		CPUMLD	1251
		0326011251 +		PL	X6,SBTX	IF END OF TABLE	CPUMLD	1252
11254	13741			BX7	X4-X1		CPUMLD	1253
		11737		BX7	X3*X7		CPUMLD	1254
		0317011253 +		NZ	X7,SBT1	IF NO MATCH ON BLOCK NAME	CPUMLD	1255
11255	0400011251 +			EQ	SBTX	RETURN	CPUMLD	1256
		**		ZIO - READ ONE BLOCK FROM DISK FILE.			CPUMLD	1258
		*					CPUMLD	1259
		*		USES	A - 1, 4, 6, 7.		CPUMLD	1260
		*			X - 1, 4, 6, 7.		CPUMLD	1261
							CPUMLD	1262
							CPUMLD	1263
11256	00000000000000000000		ZIO	SUBR		ENTRY/EXIT	CPUMLD	1264
11257	5140011266 +			SA4	ZIOA+1	SET FIRST = IN = OUT	CPUMLD	1265
		73740		SX7	X4		CPUMLD	1266
		54741		SA7	A4+B1		CPUMLD	1267
11260	54771			SA7	A7+B1		CPUMLD	1268

11262	7120011265 + 5140011267 + 73740	55441	READ SA4 SX7	ZIOA,R ZIOA+2 X4	READ ONE BLOCK SET BUFFER LIMIT	CPUMLD CPUMLD CPUMLD	1269 1270 1271
11263	5170000125 + 7274000000		SA4 SA7 SX7	A4-B1 .RL X4+	RESET POSITION IN BUFFER	CPUMLD CPUMLD CPUMLD	1272 1273 1274
11264	5170000124 + 0200011256 +		SA7 JP	.RP ZIOX	RETURN	CPUMLD CPUMLD CPUMLD	1275 1276 1277
11265		ZIOA *LG0	BSS FILEB	0 RBUF+1,RBUFL-1,(FET=5)		CPUMLD CPUMLD	1278 1279
11265	14071700000000000001		VFD	42/0LLG0,18/1		CPUMLD	1280
11266	000000000000000011310 +		VFD	42/,18/RBUF+1		CPUMLD	1281
11267	000000000000000011310 +		VFD	42/,18/RBUF+1		CPUMLD	1282
11270	000000000000000011310 +		VFD	42/,18/RBUF+1		CPUMLD	1283
11271	000000000000000012310 +		VFD	42/,18/RBUF+RBUFL-1		CPUMLD	1284

11272		CTEXT	COMCCIO - I/O FUNCTION PROCESSOR.	COMCCIO	1
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1412THE

** PRS - PRESET.

CPUMLD 1289

*

CPUMLD 1290

*

ENTRY X2, X4 AND X5 CONTAIN INFORMATION FOR *CPUMTR*.

252L678 22

*

THEY ARE SAVED AND RESTORED.

CPUMLD 1292

*

(X4) = 0 IF BATCH MODE.

CPUMLD1 2

11307 00000000000000000000 PRS

SUBR

ENTRY/EXIT

CPUMLD 1295

11310 76630

SX6

B3

SAVE REGISTERS

CPUMLD 1296

76740

SX7

B4

CPUMLD 1297

5160000115 +

SA6

.ER

CPUMLD 1298

11311 54761

SA7

A6+B1

CPUMLD 1299

76650

SX6

B5

CPUMLD 1300

54671

SA6

A7+B1

CPUMLD 1301

10655

BX6

X5

CPUMLD 1303

11312 22704

LX7

X4

CPUMLD 1305

54661

SA6

A6+B1

CPUMLD 1306

54761

SA7

A6+B1

252L678 23

10633

BX6

X3

CPUMLD 1308

11313 22702

LX7

X2

CPUMLD 1309

54671

SA6

A7+B1

CPUMLD 1310

54761

SA7

A6+B1

CPUMLD 1311

10744

BX7

X4

CPUMLD 1312

11314 5170000310 +

SA7

.BC

CPUMLD1 3

0317011324 +

NZ

X7,PRS1

IF NOT BATCH MODE

CPUMLD 1314

11315 7170024000

SX7

LBUF

SET *CPUMTR* ORIGIN TO 20000B

CPUMLD 1315

5170011602 +

SA7

OB

CPUMLD 1316

*

GET ENOUGH MEMORY FOR FILE BUFFERS AND CPUMTR.

CPUMLD 1317

*

MEMORY CM,PRSA,R,LBUF+50000B

CPUMLD 1318

11316 7160220614

CPUMLD 1319

*

REWIND *LGO* FILE.

CPUMLD 1320

11322 7120011265 +

CPUMLD 1321

CPUMLD 1322

CPUMLD 1323

CPUMLD 1324

CPUMLD 1325

CPUMLD 1326

CPUMLD 1327

CPUMLD 1328

CPUMLD 1329

CPUMLD 1330

CPUMLD 1331

CPUMLD 1332

CPUMLD 1333

CPUMLD 1334

CPUMLD 1335

CPUMLD 1336

CPUMLD 1337

CPUMLD 1338

CPUMLD 1339

CPUMLD 1340

CPUMLD 1342

CPUMLD 1343

CPUMLD 1344

*

REWIND ZIOA,R REWIND CPUMTR BINARY FILE

11324 0100011524 +

PRS1

RJ

REL

RELOCATE VARIABLE VALUES

CPUMLD 1327

11325 0100011336 +

RJ

DLB

DETERMINE LOAD BLOCKS

CPUMLD 1328

*

CLEAR BLOCK POSITION TABLE.

CPUMLD 1329

11326 13666

BX6

X6-X6

CPUMLD 1330

6160000227 +

SB6

.CP+1

CPUMLD 1331

11327 6176000056

SB7

B6+.B0L-2

CPUMLD 1332

11330 66661

PRS2

SB6

B6+B1

CPUMLD 1333

56660

SA6

B6

CPUMLD 1334

0567011330 +

NE

B6,B7,PRS2

IF MORE TO CLEAR

CPUMLD 1335

11331 0400011307 +

EQ

PRXS

RETURN

CPUMLD 1336

11332

1

PRSA

BSS

1

CPUMLD 1337

*

DBL - DISABLE BLOCK LOAD.

CPUMLD 1338

*

ENTRY (X1) = BLOCK CONTROL WORD.

CPUMLD 1339

*

ENTRY (X1) = BLOCK CONTROL WORD.

CPUMLD 1340

*

ENTRY (X1) = BLOCK CONTROL WORD.

CPUMLD 1341

*

ENTRY (X1) = BLOCK CONTROL WORD.

CPUMLD 1342

*

ENTRY (X1) = BLOCK CONTROL WORD.

CPUMLD 1343

*

ENTRY (X1) = BLOCK CONTROL WORD.

CPUMLD 1344

*

ENTRY (X1) = BLOCK CONTROL WORD.

CPUMLD 1345

*

ENTRY (X1) = BLOCK CONTROL WORD.

CPUMLD 1346

*

ENTRY (X1) = BLOCK CONTROL WORD.

CPUMLD 1347

*

ENTRY (X1) = BLOCK CONTROL WORD.

CPUMLD 1348

*

ENTRY (X1) = BLOCK CONTROL WORD.

CPUMLD 1349

*

ENTRY (X1) = BLOCK CONTROL WORD.

CPUMLD 1350

*

ENTRY (X1) = BLOCK CONTROL WORD.

CPUMLD 1351

*

ENTRY (X1) = BLOCK CONTROL WORD.

CPUMLD 1352

*

ENTRY (X1) = BLOCK CONTROL WORD.

CPUMLD 1353

*

ENTRY (X1) = BLOCK CONTROL WORD.

CPUMLD 1354

CPUMLD	1345
CPUMLD	1346
CPUMLD	1347
CPUMLD	1348
CPUMLD	1349
CPUMLD	1350
CPUMLD	1351
CPUMLD	1352
CPUMLD	1353
CPUMLD	1354
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CPUMLD	1359
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CPUMLD	1361
CPUMLD	1362
CPUMLD	1363
CPUMLD	1364
CPUMLD	1365
CPUMLD	1366
CPUMLD	1367
CPUMLD	1368
CPUMLD	1369
CPUMLD	1370
NS21000	10
NS21000	11
252L678	24
CPUMLD	1374
CPUMLD	1375
252L678	25
252L678	26
NS21000	15
NS21000	16
252L678	27
252L678	28
252L678	29
252L678	30
NS21000	19
NS21000	20
252L678	31
NS21000	22
252L678	32
NS21000	24
252L678	33
252L678	34
252L678	35
252L678	36
252L678	37
252L678	38
252L678	39
252L678	40
252L678	41

CPUMLD	1359
CPUMLD	1360
CPUMLD	1361
CPUMLD	1362
CPUMLD	1363
CPUMLD	1364
CPUMLD	1365
CPUMLD	1366
CPUMLD	1367
CPUMLD	1368
CPUMLD	1369
CPUMLD	1370
NS21000	10
NS21000	11
252L678	24
CPUMLD	1374
CPUMLD	1375
252L678	25
252L678	26
NS21000	15
NS21000	16
252L678	27
252L678	28
252L678	29
252L678	30
NS21000	19
NS21000	20
252L678	31
NS21000	22
252L678	32
NS21000	24
252L678	33
252L678	34
252L678	35
252L678	36
252L678	37
252L678	38
252L678	39
252L678	40
252L678	41

CPUMLD	1359
CPUMLD	1360
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CPUMLD	1363
CPUMLD	1364
CPUMLD	1365
CPUMLD	1366
CPUMLD	1367
CPUMLD	1368
CPUMLD	1369
CPUMLD	1370
NS21000	10
NS21000	11
252L678	24
CPUMLD	1374
CPUMLD	1375
252L678	25
252L678	26
NS21000	15
NS21000	16
252L678	27
252L678	28
252L678	29
252L678	30
NS21000	19
NS21000	20
252L678	31
NS21000	22
252L678	32
NS21000	24
252L678	33
252L678	34
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252L678	36
252L678	37
252L678	38
252L678	39
252L678	40
252L678	41

CPUMLD	1359
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CPUMLD	1367
CPUMLD	1368
CPUMLD	1369
CPUMLD	1370
NS21000	10
NS21000	11
252L678	24
CPUMLD	1374
CPUMLD	1375
252L678	25
252L678	26
NS21000	15
NS21000	16
252L678	27
252L678	28
252L678	29
252L678	30
NS21000	19
NS21000	20
252L678	31
NS21000	22
252L678	32
NS21000	24
252L678	33
252L678	34
252L678	35
252L678	36
252L678	37
252L678	38
252L678	39
252L678	40
252L678	41

11355	5212000013		SA1	X2+DILL	READ EM TYPE	252L678	42
	21122		AX1	18		252L678	43
	43271		MX2	-3		NS21000	27
11356	15112		BX1	-X2*X1		NS21000	28
	7211777773		SX1	X1-4		NS21000	29
11357	0301011350 +		ZR	X1,DLB2	IF TYPE IS UEM	252L678	44
	5110000120		SA1	UEML		NS21000	31
11360	20172		LX1	59-1		NS21000	32
	0331011362 +		NG	X1,DLB5	IF SIMULTANEOUS UEM/ESM DEFINED	252L678	45
11361	5110011041 +	DLB4	SA1	SUE		252L678	46
	0100011333 +		RJ	DBL	DISABLE SIMULTANEOUS UEM/ESM BLOCK	NS21000	35
11362	5110000025	DLB5	SA1	ECRL		252L678	47
	20114		LX1	12		NS21000	37
	43014		MX0	12		252L678	48
11363	11101		BX1	X0*X1		252L678	49
	0311011370 +		NZ	X1,DLB6	IF USER EM IS DEFINED	252L678	50
11364	5110011053 +		SA1	UEC		CPUMLD	1385
	0100011333 +		RJ	DBL	DISABLE LOAD OF USER ECS BLOCK	CPUMLD	1386
11365	5110011033 +		SA1	SCPUEC		CPUMLD	1387
	0100011333 +		RJ	DBL	DISABLE LOAD OF SCP USER ECS BLOCK	CPUMLD	1388
11366	0313011370 +		NZ	X3,DLB6	IF EM EQUIPMENT DEFINED	252L678	51
	5110011040 +		SA1	EUE		NS21000	40
11367	0100011333 +		RJ	DBL	DISABLE ECS OR USER ECS BLOCK	NS21000	41
11370	5140000122	DLB6	SA4	MMFL	DETERMINE PRESENCE OF MMF	252L678	52
	10544		BX5	X4		251L664	4
	20516		LX5	59-45		283L840	3
11371	0335011373 +		NG	X5,DLB6.1	IF SHARED BUFFERED DEVICES PRESENT	283L840	4
	5110011027 +		SA1	BIOMMF		283L840	5
11372	0100011333 +		RJ	DBL	DISABLE LOAD OF *BIOMMF* BLOCK	283L840	6
11373	20573	DLB6.1	LX5	59-46-59+45		283L840	7
	0335011377 +		NG	X5,DLB7	IF LOW SPEED PORT MMF	252L678	53
11374	5110011026 +		SA1	LSPMMF		251L664	7
	0100011333 +		RJ	DBL	DISABLE LOAD OF *LSPMMF* BLOCK	251L664	8
11375	43163		MX1	-9		CPUMLD	1390
	20144		LX1	36		251L664	9
	15541		BX5	-X1*X4		CPUMLD	1392
11376	0315011404 +		NZ	X5,DLB8	IF MMF PRESENT	252L678	54
11377	5110011025 +	DLB7	SA1	MMF		252L678	55
	0100011333 +		RJ	DBL	DISABLE LOAD OF *MMF* BLOCK	251L664	11
11400	5110000025		SA1	ECRL		CPUMLD	1396
	20114		LX1	12		NS21000	42
	43014		MX0	12		252L678	56
11401	11101		BX1	X0*X1		252L678	57
	0311011404 +		NZ	X1,DLB8	IF USER EM DEFINED	252L678	58
11402	5110011036 +		SA1	ECSBUF		NS21000	44
	0100011333 +		RJ	DBL	DISABLE LOAD OF ECS BUFFER	NS21000	45
11403	5110011042 +		SA1	MMFBUF		CPUMLD	1399
	0100011333 +		RJ	DBL	DISABLE LOAD OF MMF BUFFERS	CPUMLD	1400
11404	20414	DLB8	LX4	59-47		252L678	59
	0334011410 +		NG	X4,DLB9	IF INDEPENDENT SHARED DEVICES EXIST	252L678	60
11405	5110011030 +		SA1	ISD	DISABLE ISD BLOCK	CPUMLD	1403
	0100011333 +		RJ	DBL		CPUMLD	1404
11406	0315011410 +		NZ	X5,DLB9	IF MMF PRESENT	252L678	61
	5110011024 +		SA1	ISDMMF	DISABLE ISD/MMF COMMON CODE BLOCK	CPUMLD	1406
11407	0100011333 +		RJ	DBL		CPUMLD	1407
11410	5110000113	DLB9	SA1	MABL	GET HARDWARE OPTIONS	252L678	62
	20114		LX1	59-47		NS21000	47

11411	0331011414 + 5110011013 +	NG	X1,DLB10	IF CM EXTENSION NOT PRESENT	252L678	63
11412	0100011333 +	SA1	CMU		CPUMLD	1411
11413	0400011416 +	RJ	DBL	DISABLE CMU	CPUMLD	1412
11414	20110	EQ	DLB11	CONTINUE	252L678	64
					CPUMLD	1414
		DLB10	LX1	59-39-59+47	252L678	65
	0321011416 +	PL	X1,DLB11	IF CMU PRESENT	252L678	66
11415	5110011013 +	SA1	CMU		CPUMLD	1417
	0100011520 +	RJ	SWT	SWITCH TO *0CMU* BLOCK	CPUMLD	1418
11416	5120000045	SA2	SSTL	CHECK PRESENCE OF SYSTEM CP (SCP)	252L678	67
	20224	LX2	59-39		CPUMLD	1420
11417	0322011422 +	PL	X2,DLB12	IF SYSTEM CP (SCP) FACILITY PRESENT	252L678	68
	5110011032 +	SA1	SCP		CPUMLD	1422
11420	0100011333 +	RJ	DBL	DISABLE SCP BLOCK LOAD	CPUMLD	1423
11421	5110011033 +	SA1	SCPUEC		CPUMLD	1424
	0100011333 +	RJ	DBL	DISABLE LOAD OF SCP USER ECS BLOCK	CPUMLD	1425
11422	5110000060	SA1	ACPL	CHECK DUAL CPU STATUS	252L678	69
	54211	SA2	A1+B1		CPUMLD	1427
	12112	BX1	X1+X2		CPUMLD	1428
11423	0321011425 +	PL	X1,DLB13	IF DUAL CPU	252L678	70
	5110011021 +	SA1	DCP		CPUMLD	1430
11424	0100011333 +	RJ	DBL	DISABLE LOAD OF *DCP* BLOCK	CPUMLD	1431
11425	5110000045	SA1	SSTL	CHECK FOR SUB-CONTROL POINTS DISABLED	252L678	71
	20130	LX1	59-35		CPUMLD	1433
11426	0321011430 +	PL	X1,DLB14	IF SUBCP NOT DISABLED	252L678	72
	5110011023 +	SA1	SUBCP		CPUMLD	1435
11427	0100011333 +	RJ	DBL	DISABLE LOAD OF *SUBCP* BLOCK	CPUMLD	1436
11430	5130000113	SA3	MABL		252L678	73
	20314	LX3	59-47		NS21000	52
11431	0333011434 +	NG	X3,DLB15	IF CME NOT PRESENT	252L678	74
	5110011015 +	SA1	CMUMTR		CPUMLD	1441
11432	0100011333 +	RJ	DBL	DISABLE CMU	CPUMLD	1442
11433	0400011440 +	EQ	DLB16	CONTINUE	252L678	75
					CPUMLD	1444
11434	20312	DLB15	LX3	59-37-59+47 CHECK CPU 1 ON	252L678	76
	22213	LX2	X3,B1	CHECK CPU 0 ON	NS21000	55
	12223	BX2	X2+X3		NS21000	56
	20367	LX3	59-42-59+37	CHECK FOR CYBER 170	NS21000	57
11435	16123	BX1	-X3+X2		NS21000	58
	20303	LX3	59-39-59+42	CHECK FOR CMU	NS21000	59
	15113	BX1	-X3*X1		NS21000	60
11436	0331011440 +	NG	X1,DLB16	IF CMU .AND. (170 .OR. SINGLE CPU)	252L678	77
	5110011015 +	SA1	CMUMTR		CPUMLD	1452
11437	0100011520 +	RJ	SWT	SWITCH TO *0CMUMTR* BLOCK	CPUMLD	1453
11440	5110000045	SA1	SSTL	CHECK MASS STORAGE VALIDATION ENABLED	252L678	78
	20111	LX1	59-50		CPUMLD	1455
11441	0321011443 +	PL	X1,DLB17	IF VALIDATION ENABLED	252L678	79
	5110011031 +	SA1	VMS		CPUMLD	1457
11442	0100011333 +	RJ	DBL	DISABLE LOAD OF *VMS* BLOCK	CPUMLD	1458
11443	5130000113	SA3	MABL		252L678	80
	5120000117	SA2	EABL+DCEI	SET CONSOLE DESCRIPTOR ADDRESS	NS2332	2
11444	20244	LX2	36		NS2332	3
	43662	MX6	-10		NS2332	4
	5110000116	SA1	EABL+IOEI	SET IOU DESCRIPTOR ADDRESS	NS2332	5
11445	20122	LX1	18		NS2332	6
	53111	SA1	X1+B1	READ IOU DESCRIPTOR FROM *SAB*	NS2332	7
	20160	LX1	-12		NS2332	8

		53220	SA2	X2	READ CONSOLE DESCRIPTOR FROM *SAB*	NS2332	9
11446	7140002020		SX4	2020B	SET MASK FOR *SCD* / *MDD*	NS2332	10
	20260		LX2	-12		NS2332	11
		15616	BX6	-X6*X1	CHECK BARREL 1 PPS	NS2332	12
11447	11442		BX4	X4*X2	SET PRESENCE OF *SCD* / *MDD* IN BARREL 1	NS2332	13
	47666		CX6	X6		NS2332	14
		47444	CX4	X4	IGNORE OFF STATUS OF *SCD* / *MDD*	NS2332	15
		47444	CX4	X4	*SCD* AND *MDD* OCCUPY THE SAME PP	CPUMLD2	1
11450	37664		IX6	X6-X4		NS2332	16
	0316011457 +		NZ	X6,DLB18	IF ANY PP-S OFF	252L678	81
	10133		BX1	X3		NS2332	18
11451	20114		LX1	59-47		NS2332	19
	0331011457 +		NG	X1,DLB18	IF NOT CYBER 800	252L678	82
	20105		LX1	59-42-59+47		NS2332	21
11452	43264		MX2	-8		NS2332	22
	0321011457 +		PL	X1,DLB18	IF 865/875 CPU	252L678	83
11453	5140000114		SA4	EABL+CPEI		NS2332	24
	20422		LX4	18		NS2332	25
		53140	SA1	X4	READ CPU-0 MODEL	NS2332	26
11454	21134		AX1	28		NS2332	27
	15612		BX6	-X2*X1		NS2332	28
	7226777754		SX2	X6-0#13		NS2332	29
11455	0302011460 +		ZR	X2,DLB19	IF CYBER 810 CPU	252L678	84
	7226777753		SX2	X6-0#14		NS2332	31
11456	0302011460 +		ZR	X2,DLB19	IF CYBER 830 CPU	252L678	85
11457	5110011043 +	DLB18	SA1	CP830		252L678	86
	0100011333 +		RJ	DBL	DISABLE LOAD OF CYBER 810/830 CODE	NS2332	34
11460	43202	DLB19	MX2	2		252L678	87
	20315		LX3	59-46		NS21000	63
	11323		BX3	X2*X3		NS21000	64
		13332	BX3	X3-X2		NS21000	65
11461	0303011464 +		ZR	X3,DLB20	IF CYBER 176	252L678	88
	5110011044 +		SA1	CP176		NS21000	67
11462	0100011333 +		RJ	DBL		NS21000	68
11463	5110011003 +		SA1	XP176		CPUMLD	1460
	0100011333 +		RJ	DBL		CPUMLD	1465
11464	5120000147	DLB20	SA2	BIOL	CHECK FOR BUFFERED I/O	252L678	89
	21230		AX2	24		NS21000	70
	73220		SX2	X2		NS21000	71
11465	0312011473 +		NZ	X2,DLB22	IF BUFFERED I/O IN USE	252L678	90
	5110011047 +		SA1	BUFIO		NS21000	73
11466	0100011333 +		RJ	DBL		CPUMLD	1467
11467	5110011046 +		SA1	IHPFMD		NS21000	74
	0100011333 +		RJ	DBL		NS21000	75
11470	5110011004 +	DLB21	SA1	XP819		252L678	91
	0100011333 +		RJ	DBL		NS21000	77
11471	5110011045 +		SA1	IH819		CPUMLD	1468
	0100011333 +		RJ	DBL		CPUMLD	1469
11472	0400011475 +		EQ	DLB23	CONTINUE	252L678	92
						NS21000	79
11473	0313011470 +	DLB22	NZ	X3,DLB21	IF NOT CYBER 176 (NO 819-S)	252L678	93
	5110011046 +		SA1	IHPFMD		NS21000	81
11474	0100011333 +		RJ	DBL		CPUMLD	1471
11475	5110000045	DLB23	SA1	SSTL	CHECK DATA PROBE	252L678	94
	20135		LX1	59-30		CPUMLD	1481
11476	0321011500 +		PL	X1,DLB24	IF DATA PROBE ENABLED	252L678	95
	5110011050 +		SA1	PROBE	DISABLE DATA PROBE SEGMENT	CPUMLD	1483

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** REL - RELOCATE VALUES USED IN THE *T* INSTRUCTIONS.

CPUMLD	1508
CPUMLD	1509
CPUMLD	1510
CPUMLD	1511
CPUMLD	1512
CPUMLD	1513
CPUMLD	1514
CPUMLD	1515
CPUMLD	1516
NS22000	8
NS22000	9
NS22000	10
NS22000	11
NS22000	12
NS22000	13
CPUMLD	1519
NS21000	88
NS22000	14
CPUMLD	1522
CPUMLD	1523
NS21000	89
CPUMLD	1525
CPUMLD	1526
CPUMLD	1527
CPUMLD	1528
CPUMLD	1529
CPUMLD	1530
CPUMLD	1531
NS21000	90
CPUMLD	1533
CPUMLD	1534
CPUMLD	1535
CPUMLD	1536
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CPUMLD	1538
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CPUMLD	1555
CPUMLD	1556
CPUMLD	1557
CPUMLD	1558
CPUMLD	1559
CPUMLD	1560

11524	00000000000000000000	REL
11525	5110000310 +	
	6170000030	
11526	0301011537 +	
	5120000072	
11527	43360	
	20230	
	7262000000	
11530	20214	
	15223	
	76311	
	42223	
11531	43763	
	5110000147	
	36262	
11532	73610	
	37227	
	21152	
	11772	
11533	5160011603 +	
	5170011055 +	
11534	5160011602 +	
	5120011604 +	
11535	43714	
	36621	
	5170000057	
11536	54620	
11537	5117011551 +	REL1
	67771	
	26461	
11540	53210	
	20136	
	73310	
	73110	
11541	20436	
	0321011542 +	
	14111	
11542	53110	REL2
	0324011543 +	
	21130	
11543	0323011544 +	REL3
	14111	
11544	43752	REL4
	15117	
	6156777703	
11545	23252	
	15327	
	11272	
	73330	
11546	36331	
	15137	
	11373	
	20352	
11547	36331	

SUBR	ENTRY/EXIT
SA1	.BC
SB7	TRLAL-1
ZR	X1,REL1
SA2	ESTP
MX3	-12
LX2	24
SX6	X2+EQDE
LX2	12
BX2	-X3*X2
R=	X3,ESTE
IX2	X2*X3
MX7	-9
SA1	BIOL
IX2	X6+X2
SX6	X1
IX2	X2-X7
AX1	48-6
BX7	X7*X2
SA6	MBUF
SA7	SPB0
SA6	OB
SA2	PBUF
MX7	12
IX6	X2+X1
SA7	CMCL
SA6	A2
SA1	TRLA+B7
SB7	B7-B1
UX4,B6	X1
SA2	X1
LX1	-30
SX3	X1
SX1	X1
LX4	59-29
PL	X1,REL2
BX1	-X1
SA1	X1
PL	X4,REL3
AX1	24
PL	X3,REL4
BX1	-X1
MX7	-18
BX1	-X7*X1
SB5	B6-60
AX2	X2,B5
BX3	-X7*X2
BX2	X7*X2
SX3	X3
IX3	X3+X1
BX1	-X7*X3
BX3	X7*X3
LX3	-18
IX3	X3+X1

IF BATCH MODE
LOCATE CYBER 176 EXCHANGE PACKAGE AREA
FWA EST
SIZE OF EST
SET BUFFER FOR CPUMTR ASSEMBLY
LWA + 1 OF EST
ROUND TO NEAREST 1000B
BLOCK ORIGIN OF EXCHANGE PACKAGE AREA
SET ORIGIN OF LOAD
SET ORIGIN FOR *CPUMTR* PRESET BLOCK
SET NO MOVE PENDING AT CP ZERO
SET NEXT RELOCATION WORD
SET POSITION IN WORD
GET WORD
IF POSITIVE VALUE WANTED
IF FWA WANTED
GET LWA
IF COMPLEMENT OF ADDRESS WANTED
POSITION ADDRESS
GET ADDRESS
MASK OUT ADDRESS
GENERATE NEW ADDRESS
OVERFLOW IF SUBTRACTION

12223

22662

54620

BX2

X2+X3

MERGE ADDRESS

CPUMLD

1561

LX6

X2,B6

REPOSITION INSTRUCTION

CPUMLD

1562

SA6

A2

CPUMLD

1563

11550 0670011537 +
0400011524 +

PL

B7,REL1

IF STILL MORE INSTRUCTIONS TO MODIFY

CPUMLD

1564

EQ

RELX

RETURN

CPUMLD

1565

CPUMLD

1566

*

REMOTE CODE CONTAINING THE VARIABLE TABLE/BUFFER REFERENCES

CPUMLD

1567

CPUMLD

1568

11551

TRLA

BSS

0

CPUMLD

1569

TINST

HERE

CPUMLD

1570

31

TRLAL

EQU

*-TRLA

CPUMLD

1571

CPUMLD

1572

*

VALUES USED FOR RELOCATION AT PRESET TIME.

CPUMLD

1573

CPUMLD

1574

11602 00000000000000000000 +

OB

VFD

42/0,18/ORG

CPUMLD

1575

11603 000000000000000024000

MBUF

VFD

42/0,18/LBUF

CPUMLD

1576

11604 000000000000000013000

PBUF

VFD

42/0,18//DSL/CPML*100B

CPUMLD

1577

*

BUFFERS.

CPUMLD

1579

CPUMLD

1580

CPUMLD

1581

11307 +

RBUF

EQU

PRS

RELOCATABLE TEXT BUFFER

CPUMLD

1582

1002

RBUFL

EQU

1002B

BUFFER LENGTH

CPUMLD

1583

CPUMLD

1584

12311 +

.P1

EQU

RBUF+RBUFL

CPUMLD

1585

12431

.P2

EQU

.P1-ORG+120B ALLOW FOR BATCH MODE LOAD LENGTH

251L664

21

347

ERRNG

/DSL/CPML*100B-.P2 *CPUMLD* OVERFLOW

CPUMLD

1587

CPUMLD

1588

24000

LBUF

EQU

/DSL/CPML*100B+/DSL/PRSL*100B LOAD BUFFER

CPUMLD

1589

CPUMLD

1590

11605 00000000000000000001

CON

1

CPUMLD

1591

CPUMLD

1592

11606

END

CPUMLD

CPUMLD

1593

61300B CM STORAGE USED
PARALLEL CPU ASSEMBLY2857 STATEMENTS
1.700 SECONDS1135 SYMBOLS
675 REFERENCES

000056 INVENTED SYMBOLS

SYMBOLIC REFERENCE TABLE.

ABP	10711	PROGRAM*	8/37	8/39	10/11 L
ACPL	60	NOSTEXT	27/17		
ACTR	64		1/20 D		
ARGR	2		1/20 D		
BIOL	147	NOSTEXT	28/37	30/17	
BIOMMF	11027	PROGRAM*	14/22 L	26/28	
BUFIO	11047	PROGRAM*	14/38 L	28/41	
B1=1	0				
CCDR	70		1/20 D		
CIO=	11302	PROGRAM*	23/01	24/35	
CMCL	57	NOSTEXT	30/29 S		
CME	11011	PROGRAM*	14/11 L	25/33	
CMU	11013	PROGRAM*	14/12 L	27/02	27/08

CMUMTR	11015	PROGRAM*	14/13	L	27/31	27/43						
CMUR	65		1/20	D								
CPEI	0	NOSTEXT	28/17									
CPP	11017	PROGRAM*	14/14	L	29/22							
CPPL	70	NOSTEXT	29/17									
CPUC	40	PROGRAM*	8/01		8/06	L						
CPUMLD	0	PROGRAM*	1/18	E	6/17	L						
CPUR	6	PROGRAM*	6/33	L	10/24		10/51	13/54	15/20	16/02	18/43	
CPU2	10	PROGRAM*	6/36		6/51	L						
CPU3	14	PROGRAM*	6/57		7/07	L						
CPU4	16	PROGRAM*	7/13	L	7/16		7/21					
CPU5	22	PROGRAM*	7/14		7/23	L						
CPU6	30	PROGRAM*	7/01		7/43	L						
CP176	11044	PROGRAM*	14/35	L	28/33							
CP830	11043	PROGRAM*	14/34	L	28/26							
CSE	11034	PROGRAM*	14/27	L	25/37							
CSMR	67		1/20	D								
DBL	11333	PROGRAM*	25/09	D	26/18		26/39	27/03	27/32	28/42	29/01	29/28
			25/38		26/20		26/46	27/14	27/49	28/44	29/06	
			25/44		26/23		26/48	27/16	28/27	28/46	29/11	
			25/46		26/29		26/52	27/22	28/34	28/48	29/16	
			26/11		26/33		26/55	27/27	28/36	28/53	29/23	
DBLX	11333	PROGRAM*	25/09	L	25/13							
DCEI	3	NOSTEXT	27/51									
DCP	11021	PROGRAM*	14/16	L	27/21							
DILL	13	NOSTEXT	26/01									
DLB	11336	PROGRAM*	24/38		25/27	D	29/29					
DLBX	11336	PROGRAM*	25/27	L	25/29							
DLB1	11345	PROGRAM*	25/32		25/36		25/39	L				
DLB10	11414	PROGRAM*	27/01		27/06	L						
DLB11	11416	PROGRAM*	27/04		27/07		27/10	L				
DLB12	11422	PROGRAM*	27/12		27/17	L						
DLB13	11425	PROGRAM*	27/20		27/23	L						
DLB14	11430	PROGRAM*	27/25		27/28	L						
DLB15	11434	PROGRAM*	27/30		27/35	L						
DLB16	11440	PROGRAM*	27/33		27/42		27/45	L				
DLB17	11443	PROGRAM*	27/47		27/50	L						
DLB18	11457	PROGRAM*	28/10		28/13		28/16		28/26	L		
DLB19	11460	PROGRAM*	28/23		28/25		28/28	L				
DLB2	11350	PROGRAM*	25/45	L	26/06							
DLB20	11464	PROGRAM*	28/32		28/37	L						
DLB21	11470	PROGRAM*	28/45	L	28/51							
DLB22	11473	PROGRAM*	28/40		28/51	L						
DLB23	11475	PROGRAM*	28/49		28/54	L						
DLB24	11500	PROGRAM*	28/56		29/02	L						
DLB24.1	11503	PROGRAM*	29/04		29/07	L						
DLB25	11506	PROGRAM*	29/09		29/12	L						
DLB26	11511	PROGRAM*	29/14		29/17	L						
DLB27	11514	PROGRAM*	29/21		29/24	L						
DLB28	11517	PROGRAM*	29/26		29/29	L						
DLB3	11352	PROGRAM*	25/42		25/49	L						
DLB4	11361	PROGRAM*	25/47		26/10	L						
DLB5	11362	PROGRAM*	26/09		26/12	L						
DLB6	11370	PROGRAM*	26/16		26/21		26/24	L				
DLB6.1	11373	PROGRAM*	26/27		26/30	L						
DLB7	11377	PROGRAM*	26/31		26/38	L						
DLB8	11404	PROGRAM*	26/37		26/44		26/49	L				

DLB9	11410	PROGRAM*	26/50	26/53	26/56 L			
EABL	114	NOSTEXT	27/51	27/54	28/17			
ECRL	25	NOSTEXT	26/12	26/40				
ECS	11035	PROGRAM*	13/18	14/28 L	25/43			
ECSBUF	11036	PROGRAM*	14/29 L	26/45				
EEC	11037	PROGRAM*	14/30 L	25/45				
ENOV	75	PROGRAM*	7/52	8/13 L				
ENT	10715	PROGRAM*	8/33	10/37 L				
ENT1	10721	PROGRAM*	10/46	10/48 L				
EQDE	0	NOSTEXT	30/11					
ERR	11156	PROGRAM*	19/06 D	19/10				
ERRA	11173	PROGRAM*	8/32	8/36	8/40	8/48	21/40	
			8/34	8/38	8/42	19/36 L		
ERRB	11176	PROGRAM*	8/32	19/38 L				
ERRC	11202	PROGRAM*	8/38	8/40	19/40 L			
ERRD	11205	PROGRAM*	8/34	8/36	8/42	8/48	19/42 L	
ERRX	11156	PROGRAM*	19/06 L	19/13				
ERR1	11157	PROGRAM*	6/29	10/23	19/07 L	21/46	22/06	22/14
ERR2	11165	PROGRAM*	19/08	19/16 L				
ERR3	11172	PROGRAM*	19/31 L	19/33				
ESTE	2	NOSTEXT	25/50					
ESTP	72	NOSTEXT	25/49	30/08				
EUE	11040	PROGRAM*	14/31 L	26/22				
EXML	70	NOSTEXT	25/39					
FBAE	104	PROGRAM*	8/38 L	11/12				
FIL	10723	PROGRAM*	6/51	11/11 D				
FILA	10742	PROGRAM*	11/32	12/08 L				
FILX	10723	PROGRAM*	11/11 L	11/49				
FIL1	10726	PROGRAM*	11/21 L	11/46	11/51			
FIL2	10735	PROGRAM*	11/28	11/45 L	12/03			
FIL4	10740	PROGRAM*	11/22	11/55 L				
FWPR	66		1/20 D					
IHPFMD	11046	PROGRAM*	14/37 L	28/43	28/52			
IH819	11045	PROGRAM*	14/36 L	28/47				
IOEI	2	NOSTEXT	27/54					
ISD	11030	PROGRAM*	14/23 L	26/51				
ISDMMF	11024	PROGRAM*	14/19 L	26/54				
JOPR	66		1/20 D					
LBUF	24000		24/26	24/31	31/16	31/32 D		
LDRR	67		1/20 D					
LINP	74		1/20 D					
LSPMMF	11026	PROGRAM*	14/21 L	26/32				
LTPT	75	PROGRAM*	6/34	8/30 L				
LTPT2	112	PROGRAM*	6/27	8/46 L				
LWPR	65		1/20 D					
MABL	113	NOSTEXT	25/30	26/56	27/28	27/50		
MBUF	11603	PROGRAM*	30/23 S	31/10	31/10	31/16 L		
MCE	11052	PROGRAM*	14/41 L	29/15				
MEFL	77	NOSTEXT	13/23					
MLBR	55		1/20 D					
MMF	11025	PROGRAM*	14/20 L	26/38				
MMFBUF	11042	PROGRAM*	14/33 L	26/47				
MMFL	122	NOSTEXT	26/24					
MSG=	67	PROGRAM*	8/01	19/09				
MS1W	31	NOSTEXT	19/25 S					
NCPL	70	NOSTEXT	19/16					
NVE	11022	PROGRAM*	14/17 L	29/10				

OB	11602	PROGRAM*	24/27 S	31/10	31/10	31/10	31/10	31/10	31/10	31/10	31/10
			30/25 S	31/10	31/10	31/10	31/10	31/10	31/10	31/10	31/10
			31/10	31/10	31/10	31/10	31/10	31/10	31/10	31/10	31/15 L
ORG	0	PROGRAM*	6/02 L	31/15	31/29						
PBUF	11604	PROGRAM*	30/26	31/10	31/10	31/17 L					
PCP	11020	PROGRAM*	14/15 L	29/27							
PCPP	134	NOSTEXT	29/24								
PGNR	64		1/20 D								
PID	10746	PROGRAM*	8/31	12/31 L							
PIDA	11003	PROGRAM*	12/57	13/37	14/01 L	14/46	14/50	22/34			
PIDAL	52		14/46 D	22/35							
PID1	10751	PROGRAM*	12/41 L	13/03	13/34						
PID2	10764	PROGRAM*	12/55	13/05	13/13 L						
PID3	10772	PROGRAM*	12/53	13/11	13/19	13/32 L					
PID4	10773	PROGRAM*	12/44	13/36 L							
PID5	10775	PROGRAM*	13/40 L	13/51							
PRESET	11005	PROGRAM*	14/06 L								
PROBE	11050	PROGRAM*	14/39 L	28/57							
PRS	11307	PROGRAM*	6/22	24/08 D	31/25						
PRSA	11332	PROGRAM*	24/31	24/50 L							
PRSX	11307	PROGRAM*	24/08 L	24/48							
PRS1	11324	PROGRAM*	24/25	24/37 L							
PRS2	11330	PROGRAM*	24/45 L	24/47							
RA.CEJ	66		1/20 D								
RA.CMU	65		1/20 D								
RA.MTR	1		1/20 D								
RBUF	11307	PROGRAM*	20/16	21/11	23/13	23/14	23/15	23/16	31/25 D	31/28	
RBUFL	1002		23/16	31/26 D	31/28						
REL	11524	PROGRAM*	24/37	30/04 D							
RELX	11524	PROGRAM*	30/04 L	31/05							
REL1	11537	PROGRAM*	30/07	30/31 L	31/04						
REL2	11542	PROGRAM*	30/39	30/41 L							
REL3	11543	PROGRAM*	30/42	30/44 L							
REL4	11544	PROGRAM*	30/44	30/46 L							
ROW	11210	PROGRAM*	20/04 D	21/41	22/05						
ROWX	11210	PROGRAM*	20/04 L	20/14	20/17						
ROW1	11211	PROGRAM*	20/05 L	20/19							
ROW2	11215	PROGRAM*	20/08	20/16 L							
RRB	11221	PROGRAM*	6/23	20/18	20/37 D						
RRBA	11233	PROGRAM*	6/26 S	20/45	21/10 L						
RRBB	11234	PROGRAM*	20/40	20/45	21/11 L						
RRBX	11221	PROGRAM*	20/37 L	20/47	21/05	21/08					
RRB1	11226	PROGRAM*	20/53 L	20/56							
RRB3	11220	PROGRAM*	20/35 L	20/39							
RTB	11235	PROGRAM*	6/28	6/35	21/39 D						
RTBX	11235	PROGRAM*	21/39 L	21/42	22/10						
RTB1	11240	PROGRAM*	21/45 L	21/52							
RTB2	11245	PROGRAM*	22/05 L	22/09							
RTB3	11250	PROGRAM*	22/03	22/12 L							
SBDP	11005	PROGRAM*	13/01	13/37	14/05 D						
SBT	11251	PROGRAM*	12/48	22/32 D							
SBTX	11251	PROGRAM*	22/32 L	22/38	22/42						
SBT1	11253	PROGRAM*	22/36 L	22/41							
SCP	11032	PROGRAM*	14/25 L	27/13							
SCPUEC	11033	PROGRAM*	14/26 L	26/19	27/15						
SPB0	11055	PROGRAM*	13/06	14/48 L	30/24 S						
SPBOL	11011	PROGRAM*	12/54	14/10 D	14/50						

SPBP	11056	PROGRAM*	13/02 S	13/40	14/50 L	14/51				
SPBPL	6		13/43	14/51 D						
SPPR	27		1/20 D							
SSTL	45	NOSTEXT	27/10	27/23	27/45	28/54	29/02	29/07	29/12	
SUBCP	11023	PROGRAM*	14/18 L	27/26						
SUE	11041	PROGRAM*	14/32 L	26/10						
SWT	11520	PROGRAM*	25/34	27/09	27/44	29/42 D				
SWTX	11520	PROGRAM*	29/42 L	29/53						
SYS=	43	PROGRAM*	8/03	19/14	24/31					
TBLK	11010	PROGRAM*	14/09 L							
TJMP	11007	PROGRAM*	14/08 L							
TRACE	11051	PROGRAM*	14/40 L	29/05						
TREL	11006	PROGRAM*	14/07 L							
TRLA	11551	PROGRAM*	30/31	31/09 L	31/11					
TRLAL	31		30/06	31/11 D						
TXT	11064	PROGRAM*	8/35	15/11 L						
TXTA	11077	PROGRAM*	15/32	15/52	16/12 L					
TXTB	11120	PROGRAM*	15/21	15/32	15/52	17/15 L				
TXT2	11072	PROGRAM*	15/43 L	16/01						
UEC	11053	PROGRAM*	14/42 L	26/17						
UEML	120	NOSTEXT	26/07							
VFD	11140	PROGRAM*	6/52	17/53 D						
VFDX	11140	PROGRAM*	17/53 L	18/01						
VFD1	11143	PROGRAM*	17/57 L	18/13	18/32					
VMS	11031	PROGRAM*	14/24 L	27/48						
XFAE	106	PROGRAM*	8/40 L	17/54						
XFR	11155	PROGRAM*	8/41	18/42 L						
XJPR	66		1/20 D							
XP176	11003	PROGRAM*	14/03 L	28/35						
XP819	11004	PROGRAM*	14/04 L	28/45						
ZIO	11256	PROGRAM*	20/35	22/53 D						
ZIOA	11265	PROGRAM*	22/54	23/01	23/02	23/10 L	24/35			
ZIOX	11256	PROGRAM*	22/53 L	23/08						
(RFL)	0		24/31 D							
.BC	310	PROGRAM*	6/56	9/18 L	13/04	19/07	20/38	24/24 S	25/28	30/05
.BO	146	PROGRAM*	7/17	9/09 L	10/47	12/33	13/09 S	13/48 S		
.BOL	60		8/32	10/45	11/57	12/35 S	13/17 S	18/08		
.CP	226	PROGRAM*	2/36 D	7/08	8/32	9/11	9/15	24/44		
			7/11	9/12 L	13/09 S	13/48 S	18/05			
.EP	307	PROGRAM*	7/17	11/25	13/17 S	15/19	24/43			
.ER	115	PROGRAM*	7/02 S	7/25	9/17 L	10/50 S				
.FB	311	PROGRAM*	7/27	9/04 L	24/11 S	25/35				
.FBL	10000	PROGRAM*	8/38	9/19 L	11/17					
.LL	306	PROGRAM*	2/37 D	8/38	9/19					
.P1	12311	PROGRAM*	7/07	9/16 L	13/53 S					
.P2	12431	PROGRAM*	31/28 D	31/29						
.RL	125	PROGRAM*	31/29 D	31/30						
.RP	124	PROGRAM*	6/24	9/07 D	20/06	21/04 S	23/05 S			
.TB	126	PROGRAM*	9/05 L	9/07	20/05	20/43 S	23/07 S			
.TBL	20	PROGRAM*	8/34	8/36	8/42	8/48	9/08 L	10/39	15/22	
.XF	10311	PROGRAM*	2/38 D	8/34	8/36	8/42	8/48	9/08		
.XFL	400	PROGRAM*	8/40	9/20 L	17/55					
			2/39 D	8/40	9/20					

.1

2

9/10 D	16/17	16/25 D	16/31	16/41 D	16/45	16/53 D	17/01
9/10	16/19 D	16/25	16/35 D	16/41	16/47 D	16/53	17/03 D
9/14 D	16/19	16/28 D	16/35	16/43 D	16/47	16/56 D	17/03
9/14	16/22 D	16/28	16/38 D	16/43	16/50 D	16/56	17/05 D
16/17 D	16/22	16/31 D	16/38	16/45 D	16/50	17/01 D	17/05

SYMBOL QUALIFIER = MACRO\$

ENDRUN	11163	PROGRAM*	8/03 D	19/14 D
MEMORY	11316	PROGRAM*	24/31 D	
MESSAGE	11160	PROGRAM*	8/01 D	19/09 D
READ	11260	PROGRAM*	23/01 D	
REWIND	11322	PROGRAM*	24/35 D	
SYSCOM	0	PROGRAM*	1/20 D	
SYSTEM	11316	PROGRAM*	24/31 D	

SYMBOL QUALIFIER = DSL

CPBL	60	2/36		
CPML	130	31/17	31/30	31/32
PRSL	110	31/32		



1		1
2		2
3		3
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60		60